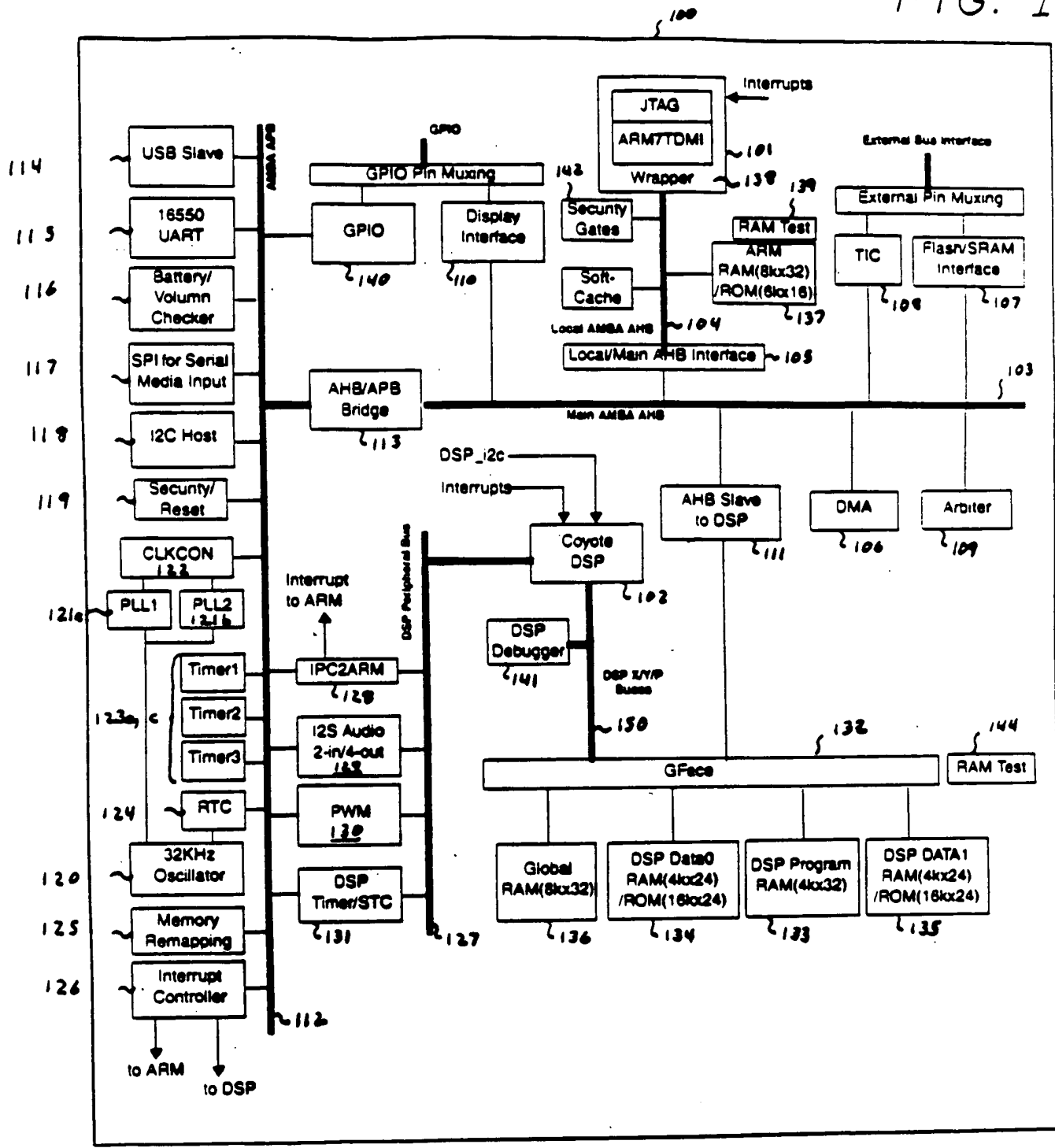


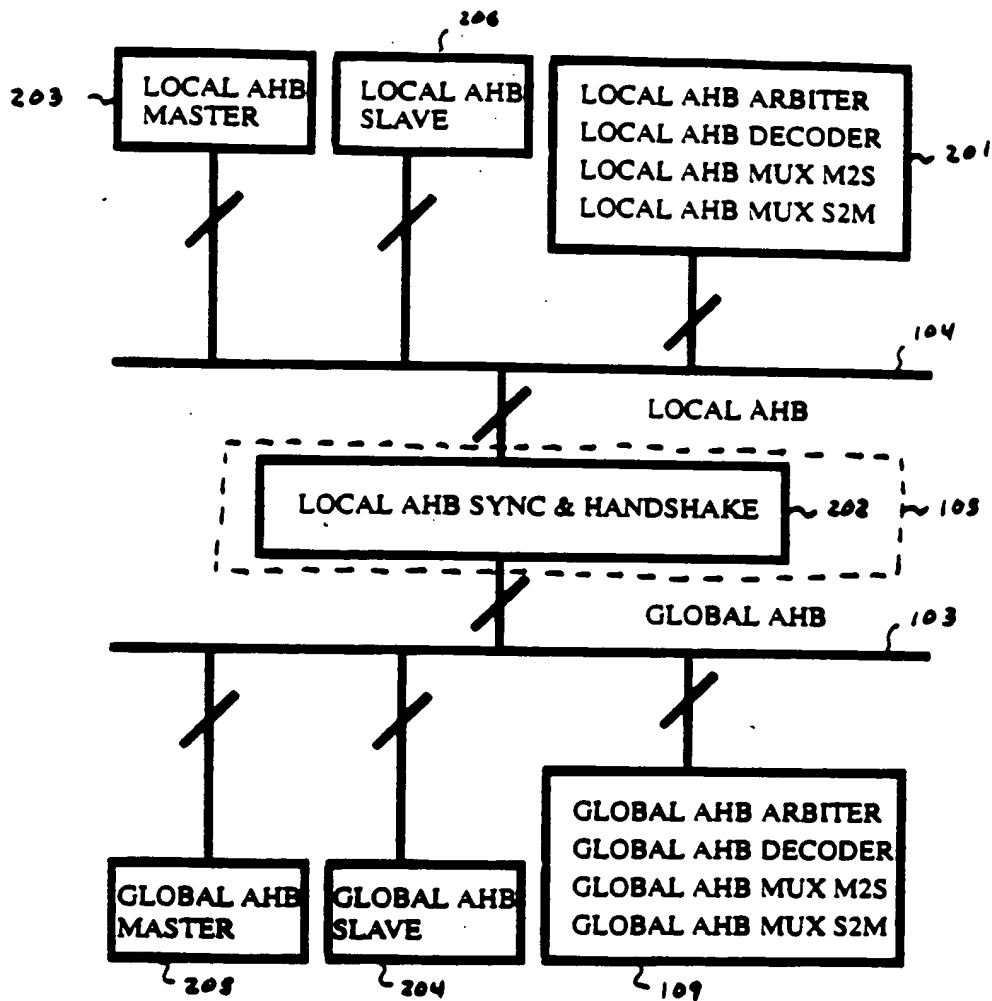
FIG. 1

FIG. 1



2836-P139US

FIG. 2A



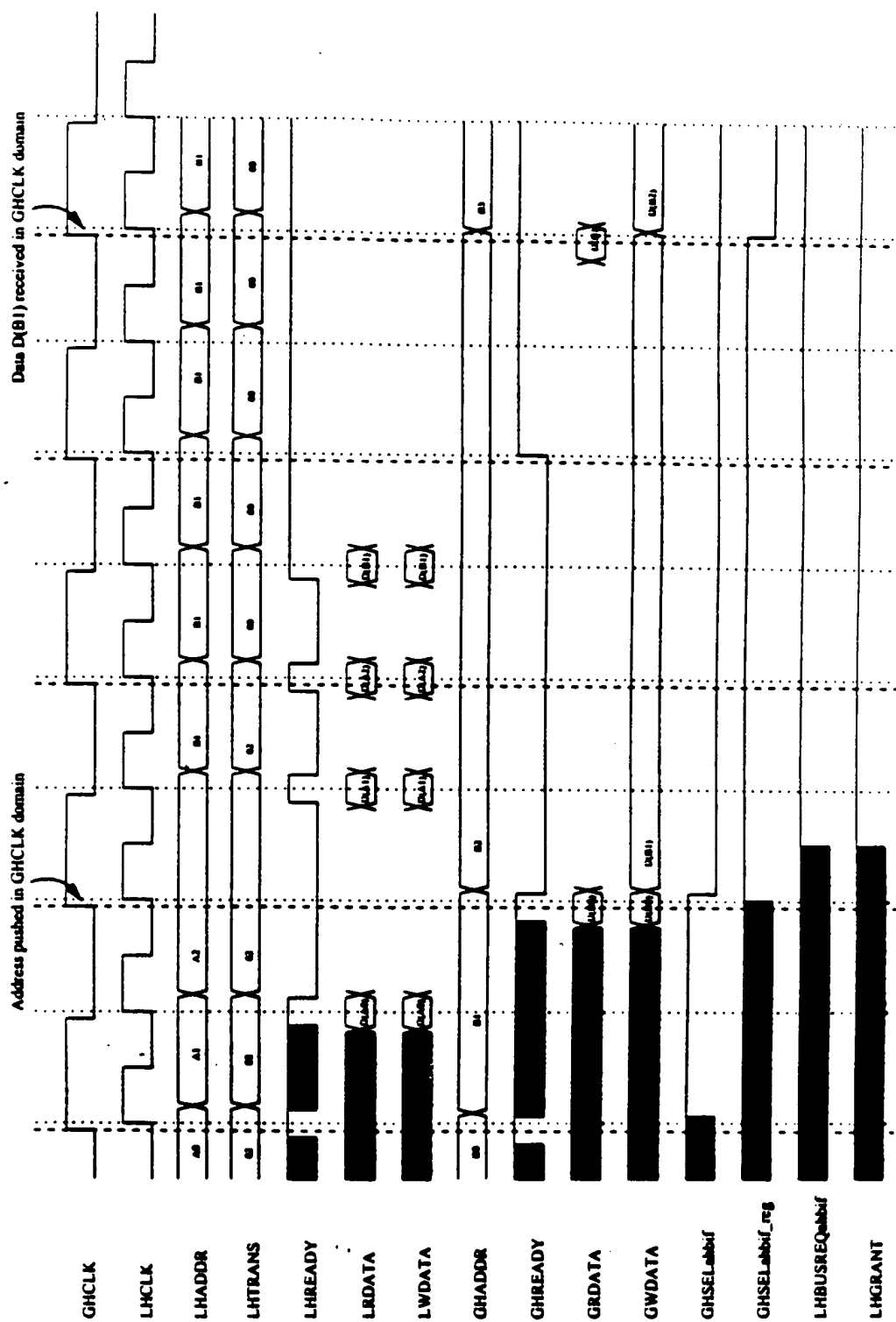


FIG. 2B

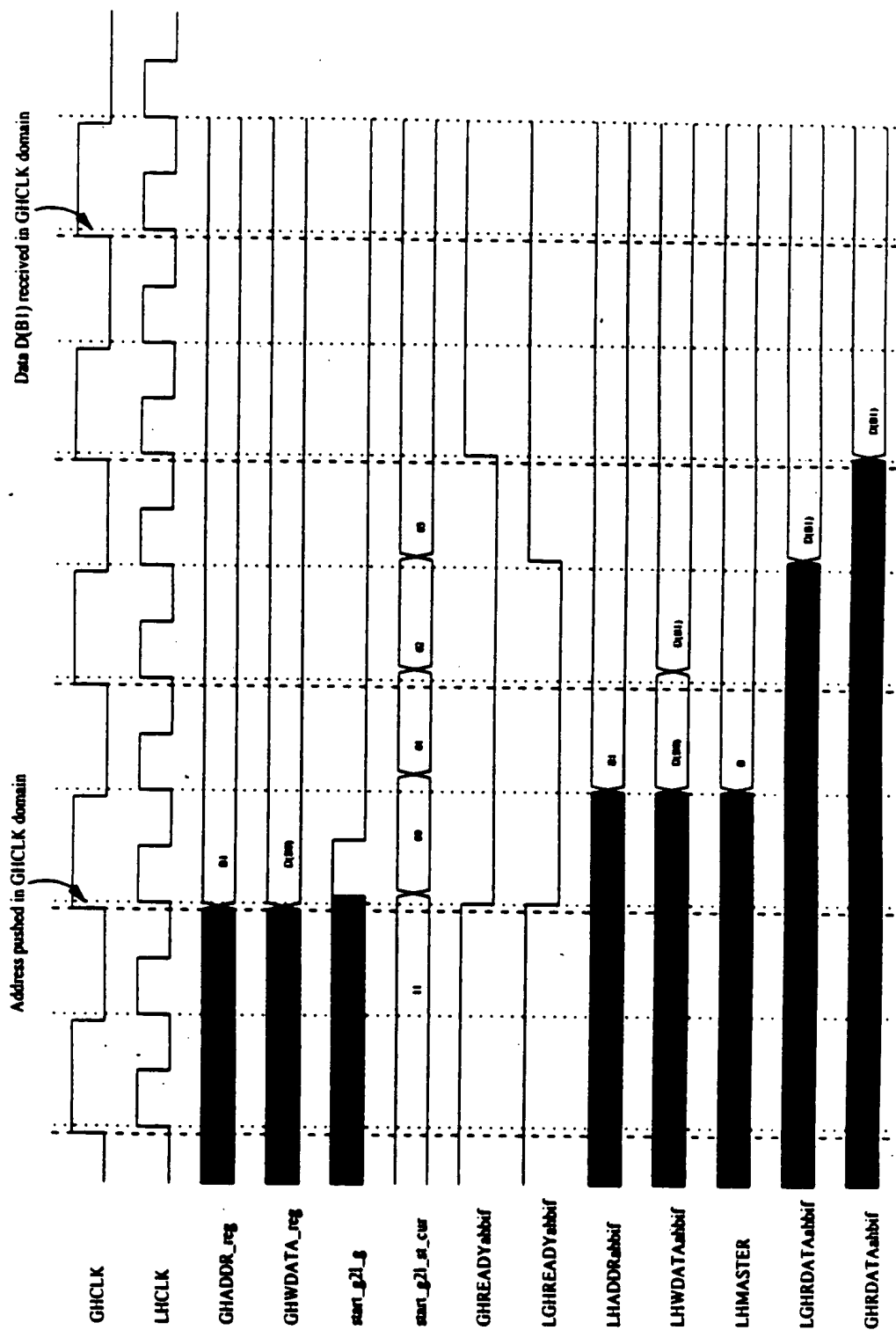


FIG. 2C

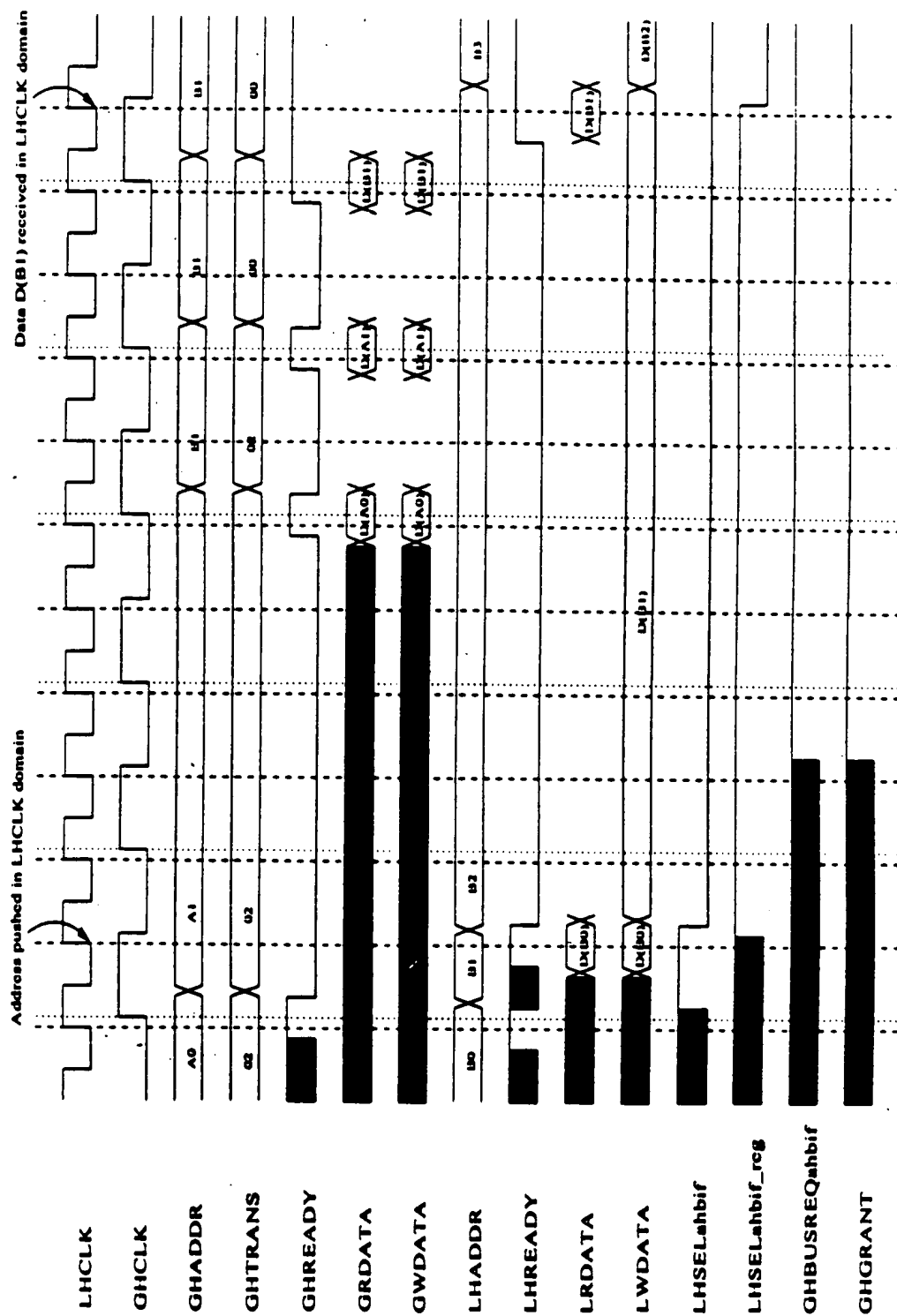


FIG. 2D

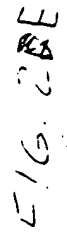
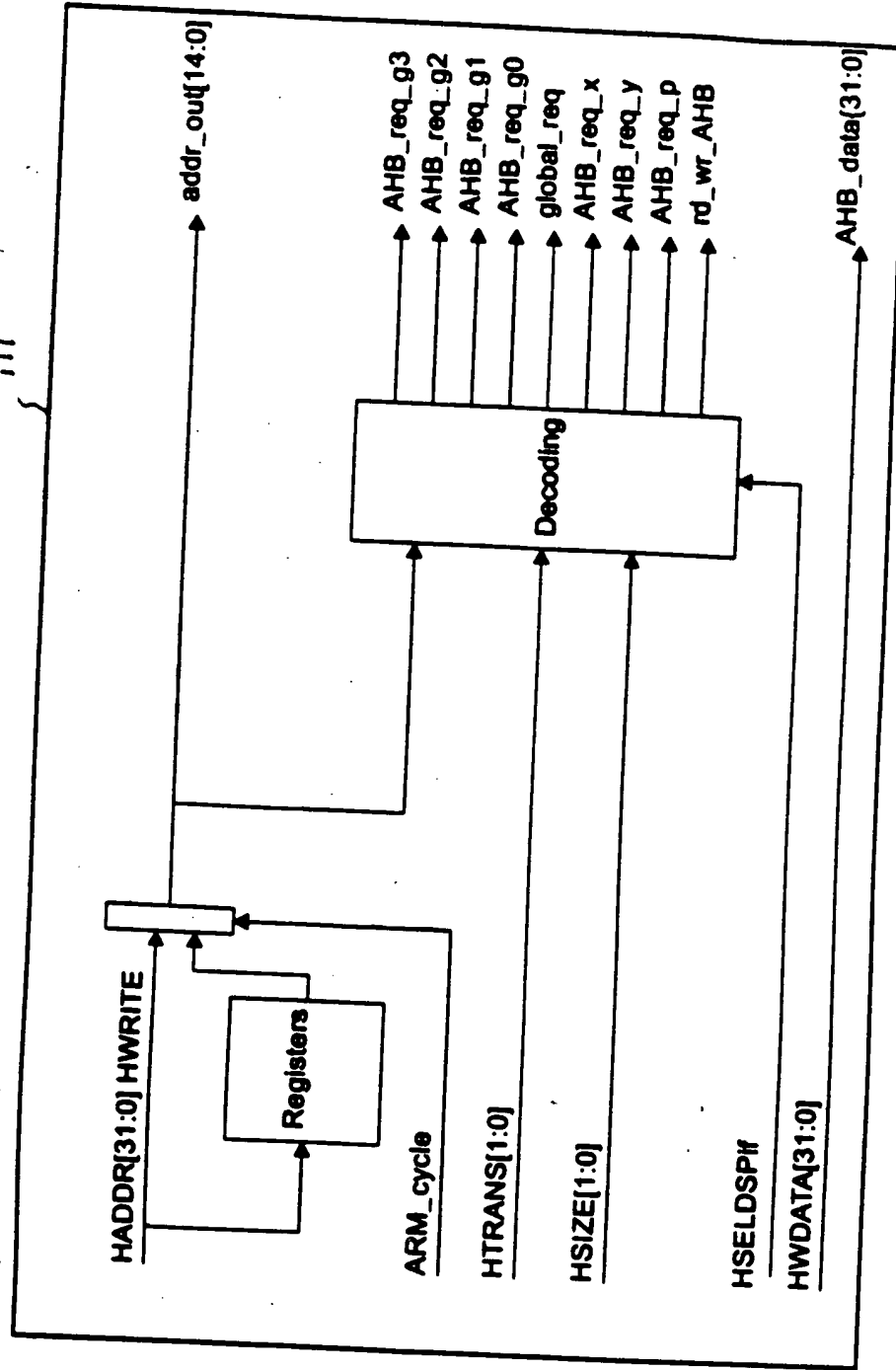


FIG. 3



132

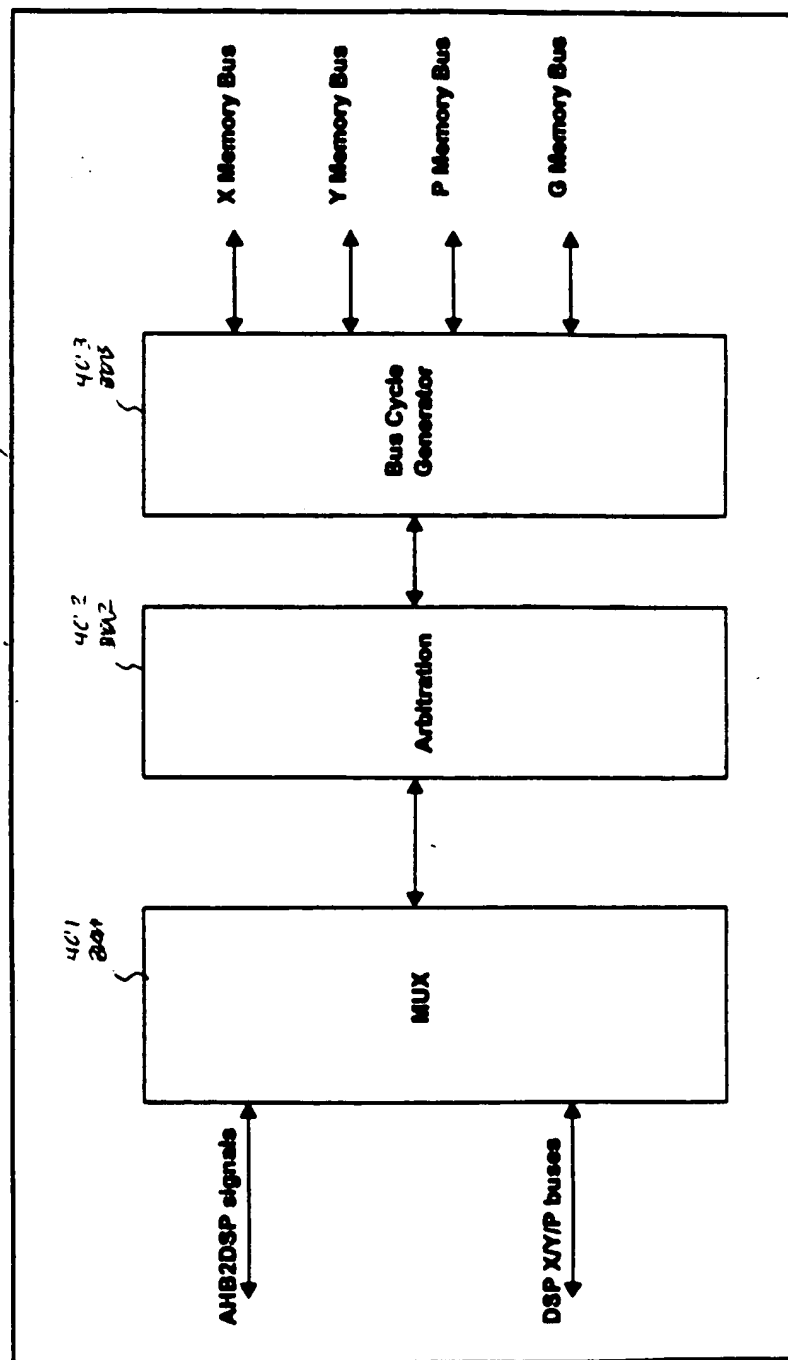
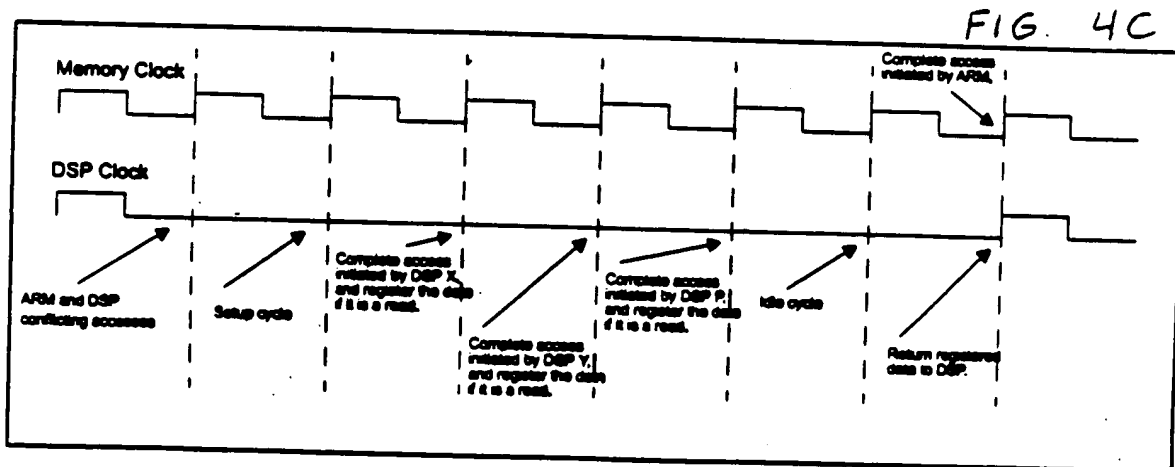
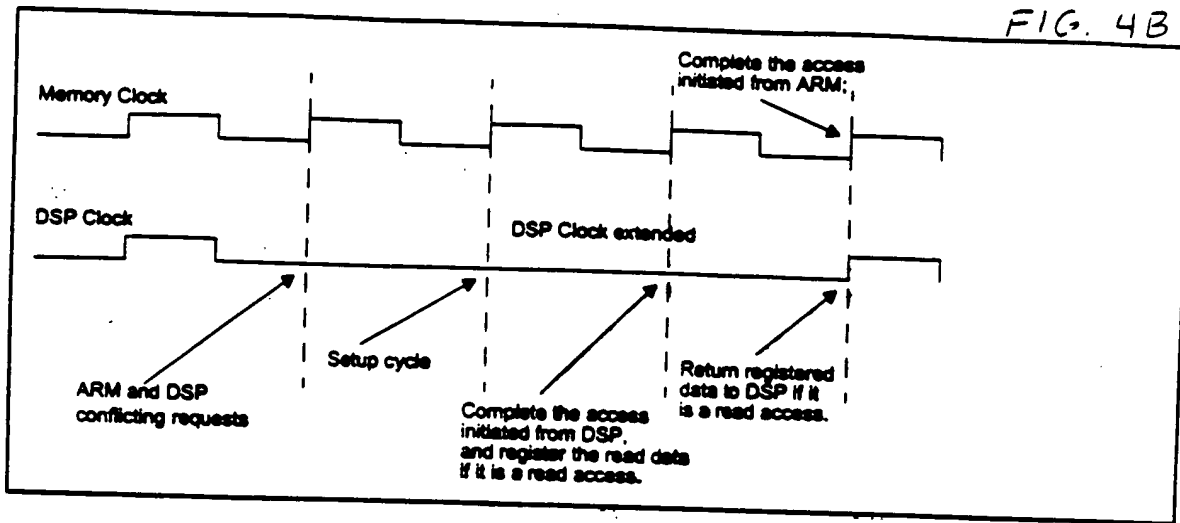
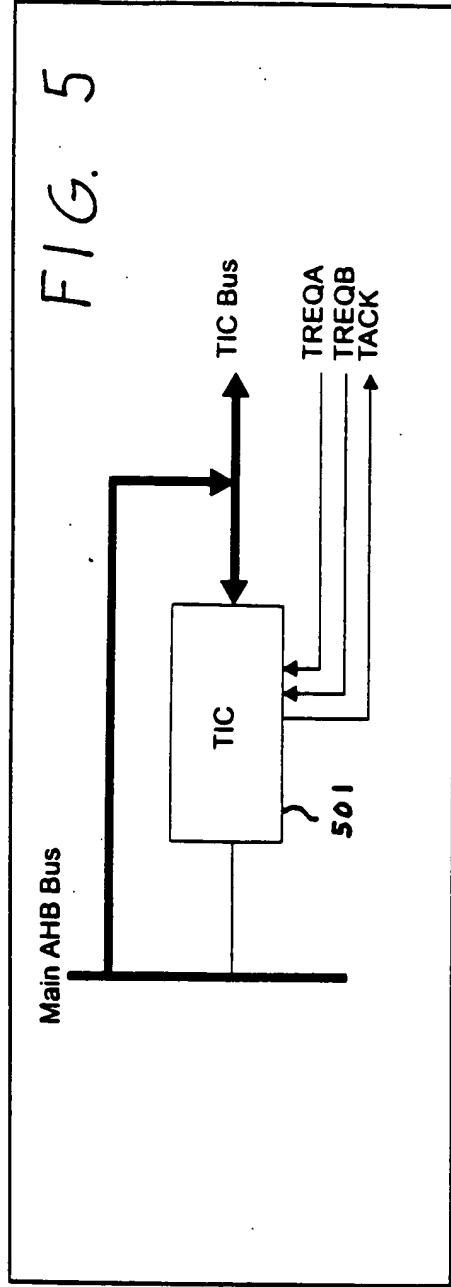
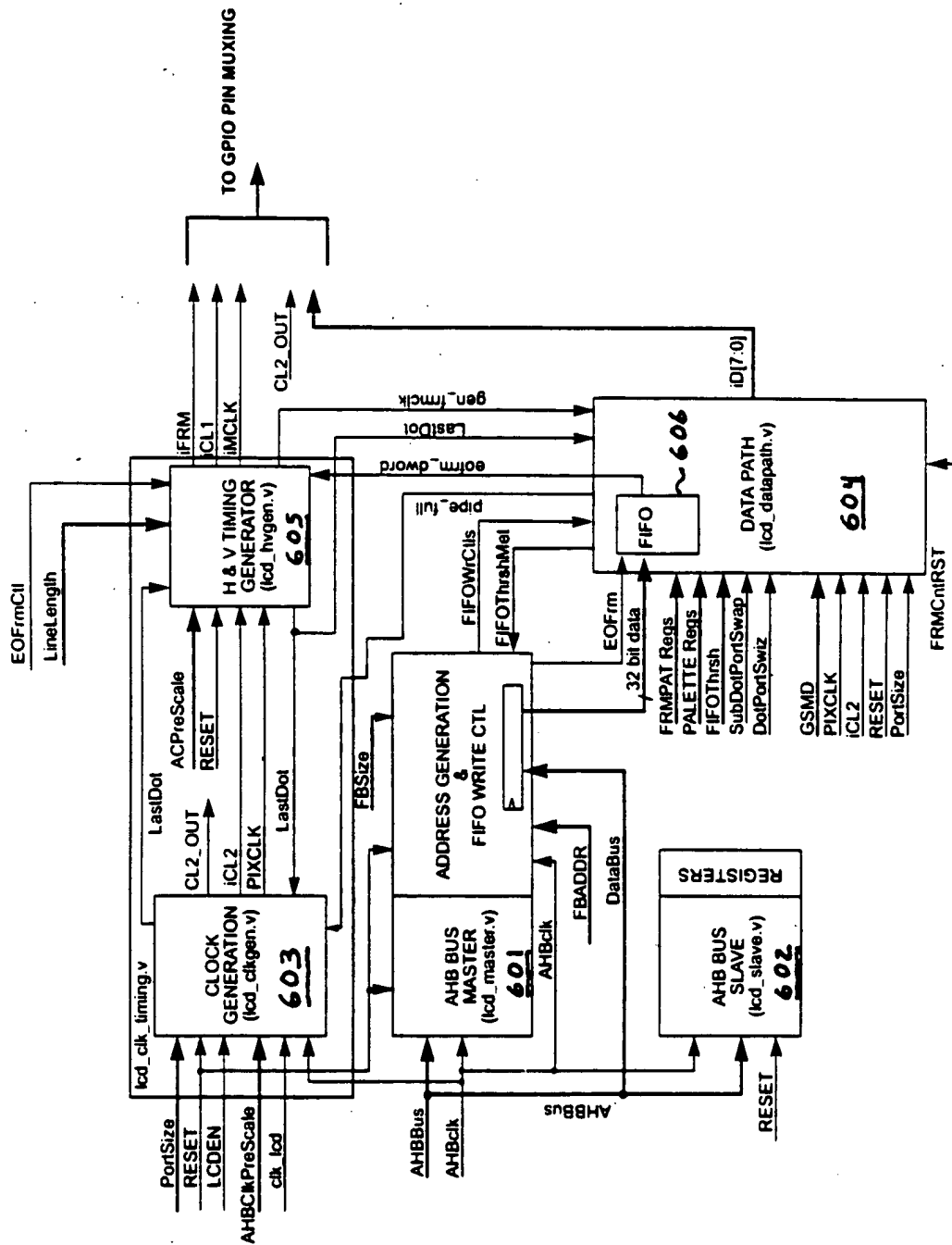


FIG. 4A







LCD Controller (lcd_top.v) Block Diagram

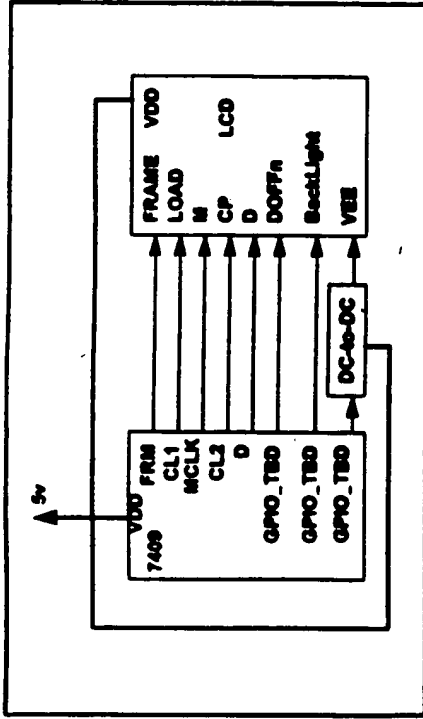


FIG 6B

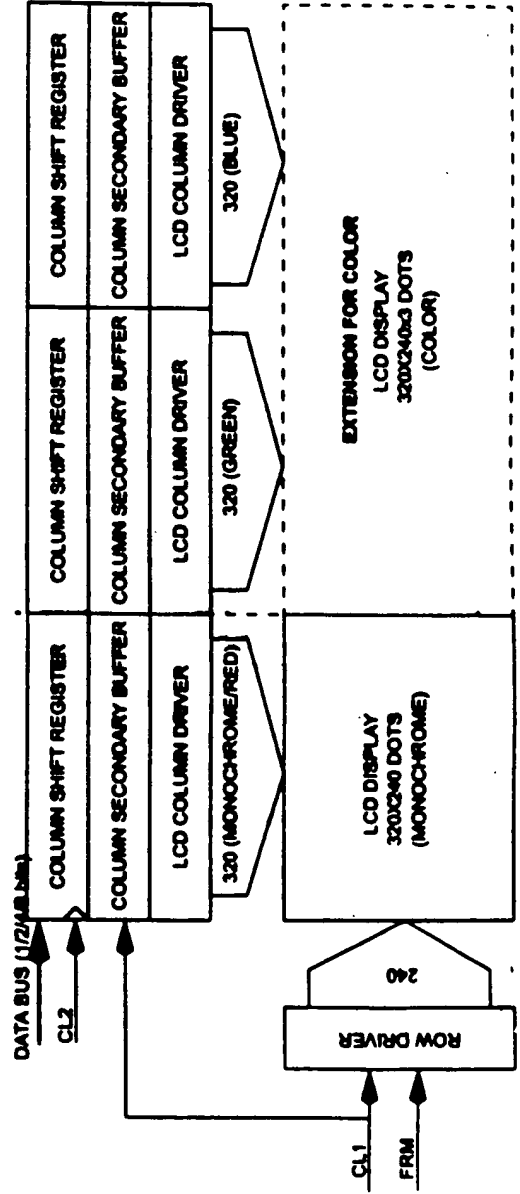


FIG. 6C

FIG 6 E

11

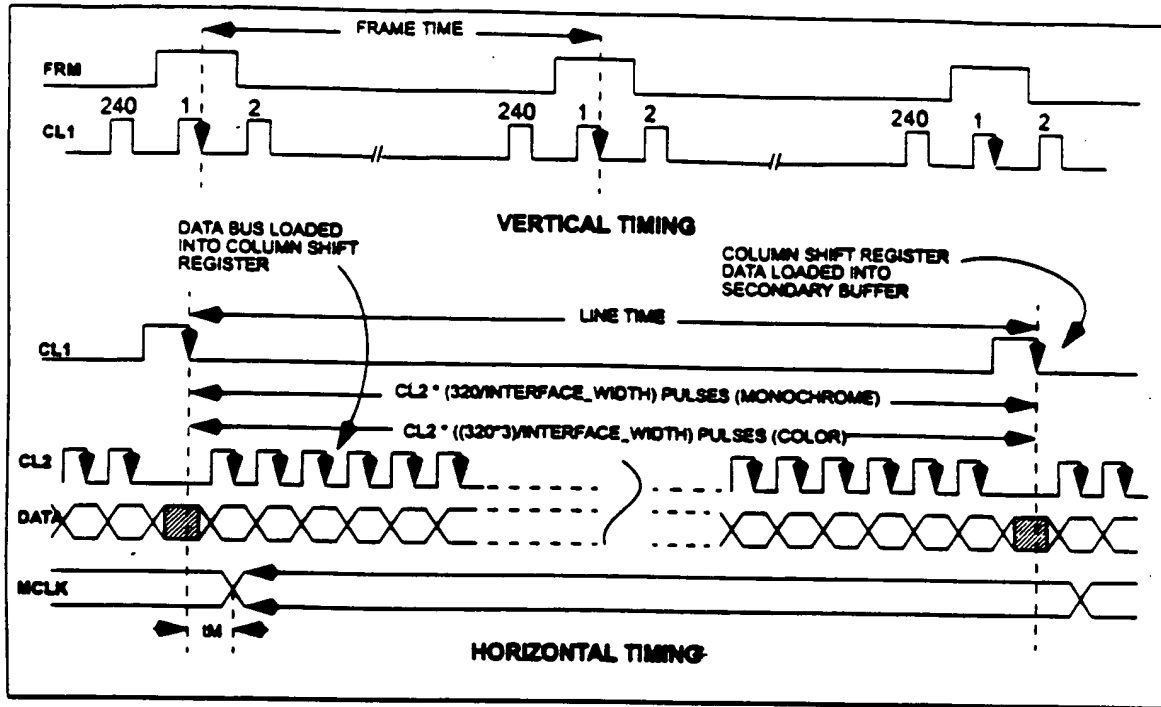


FIG 6 D

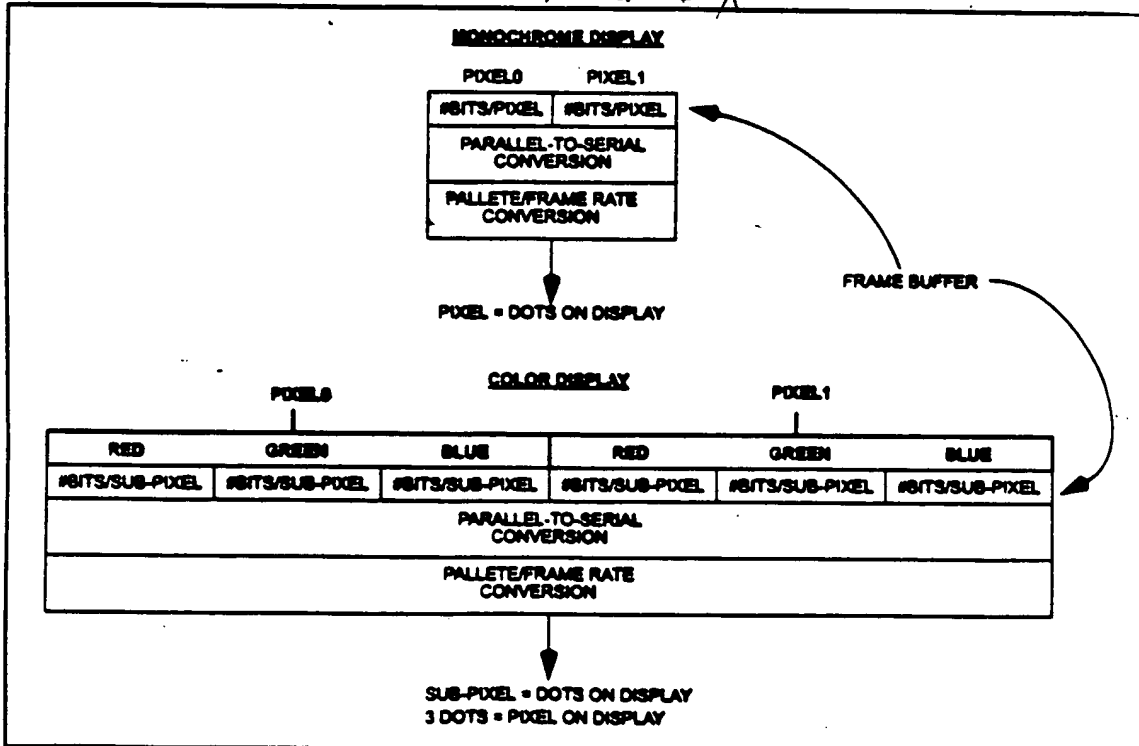


FIG 6G

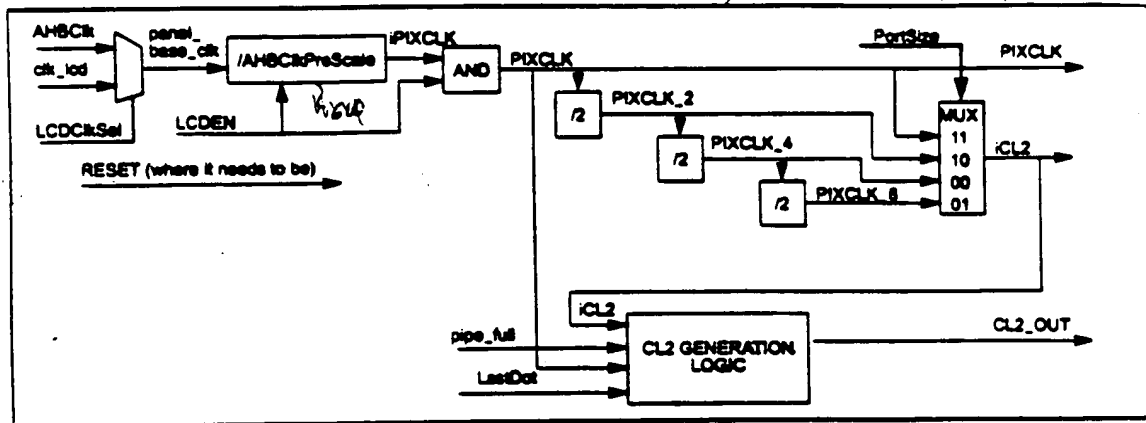


FIG 6H

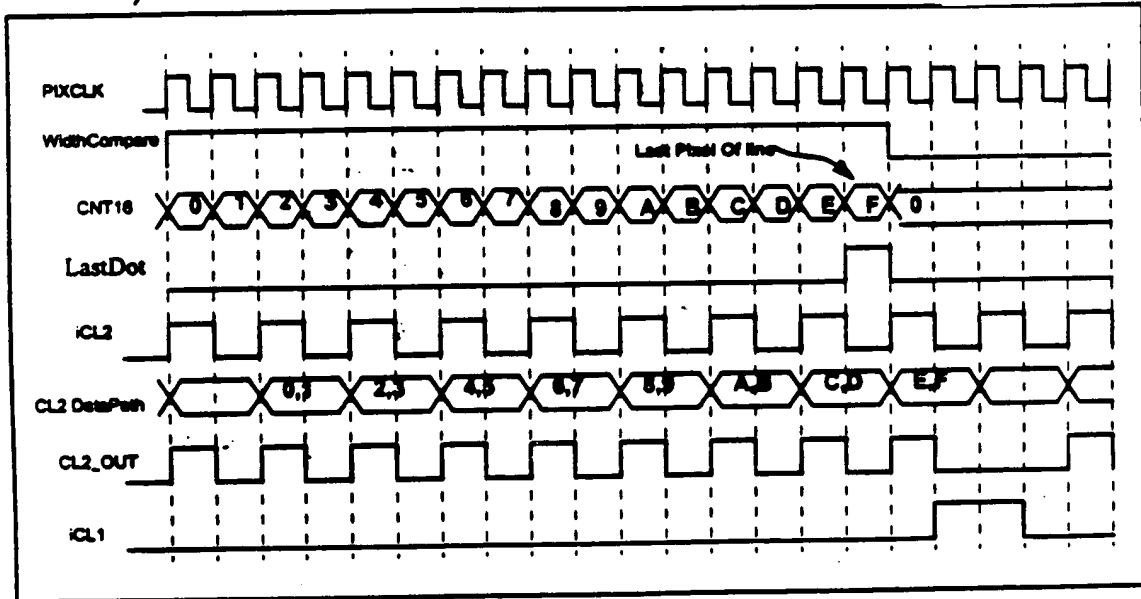


FIG 6I

605

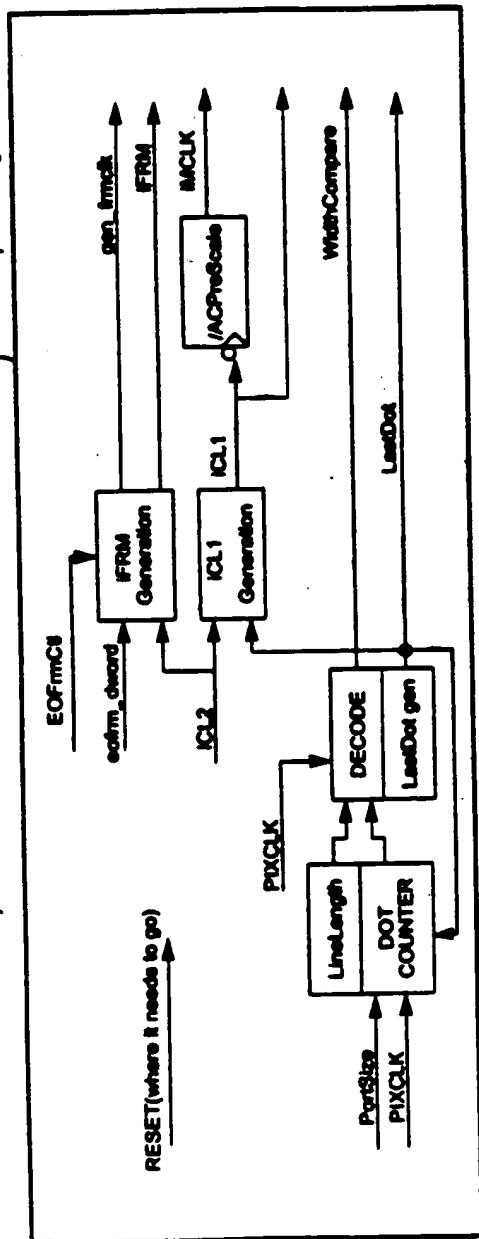


FIG 67

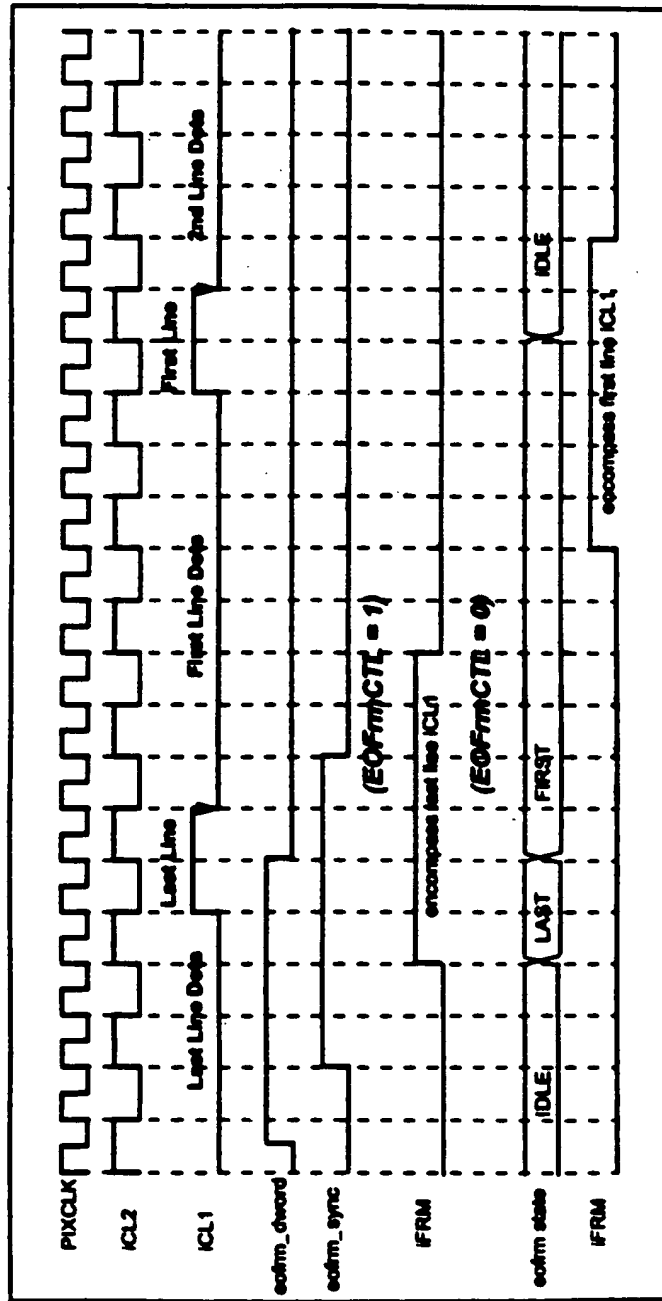
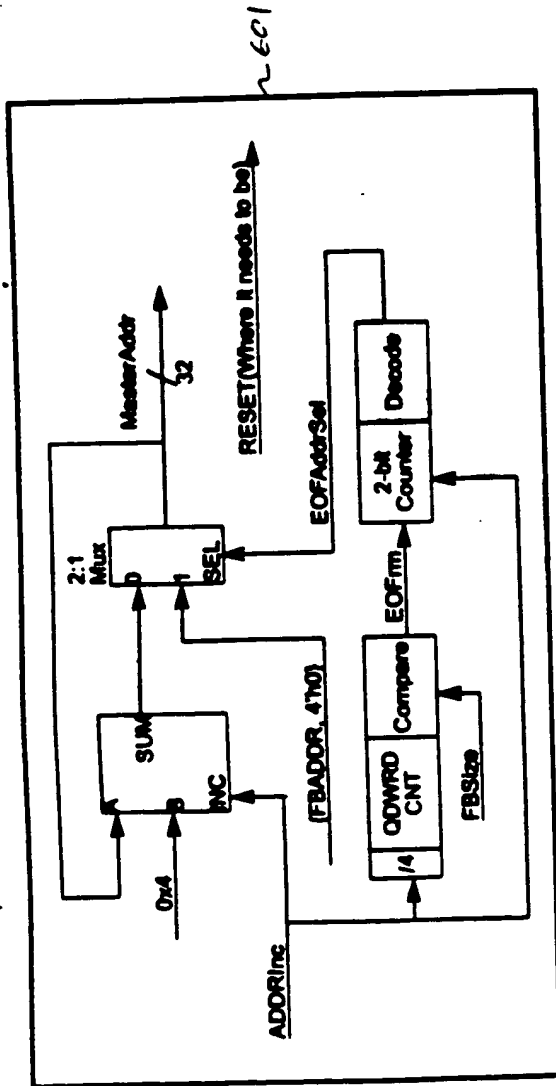


FIG. 6K



~601

FIG 6L

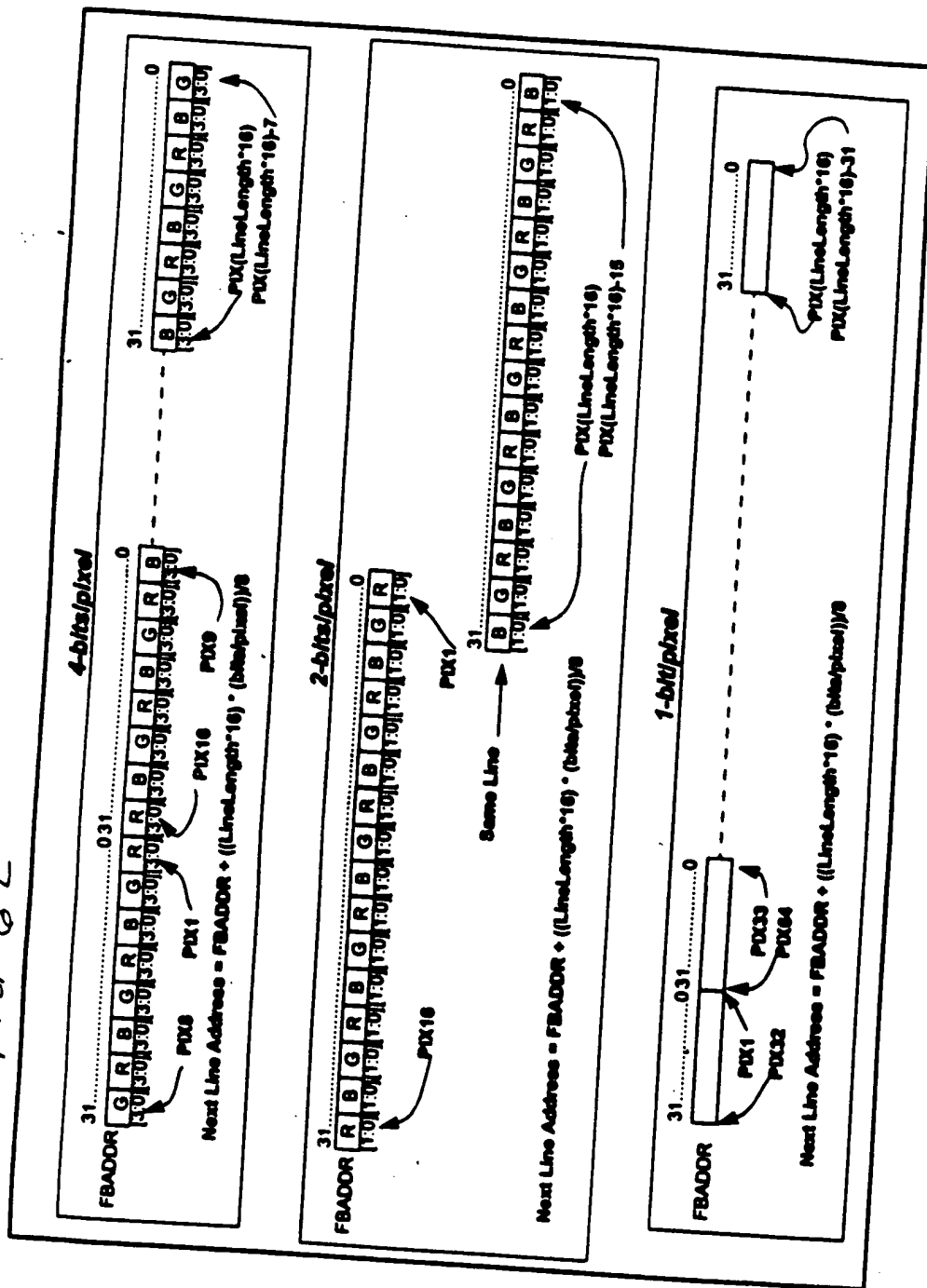


FIG 6M

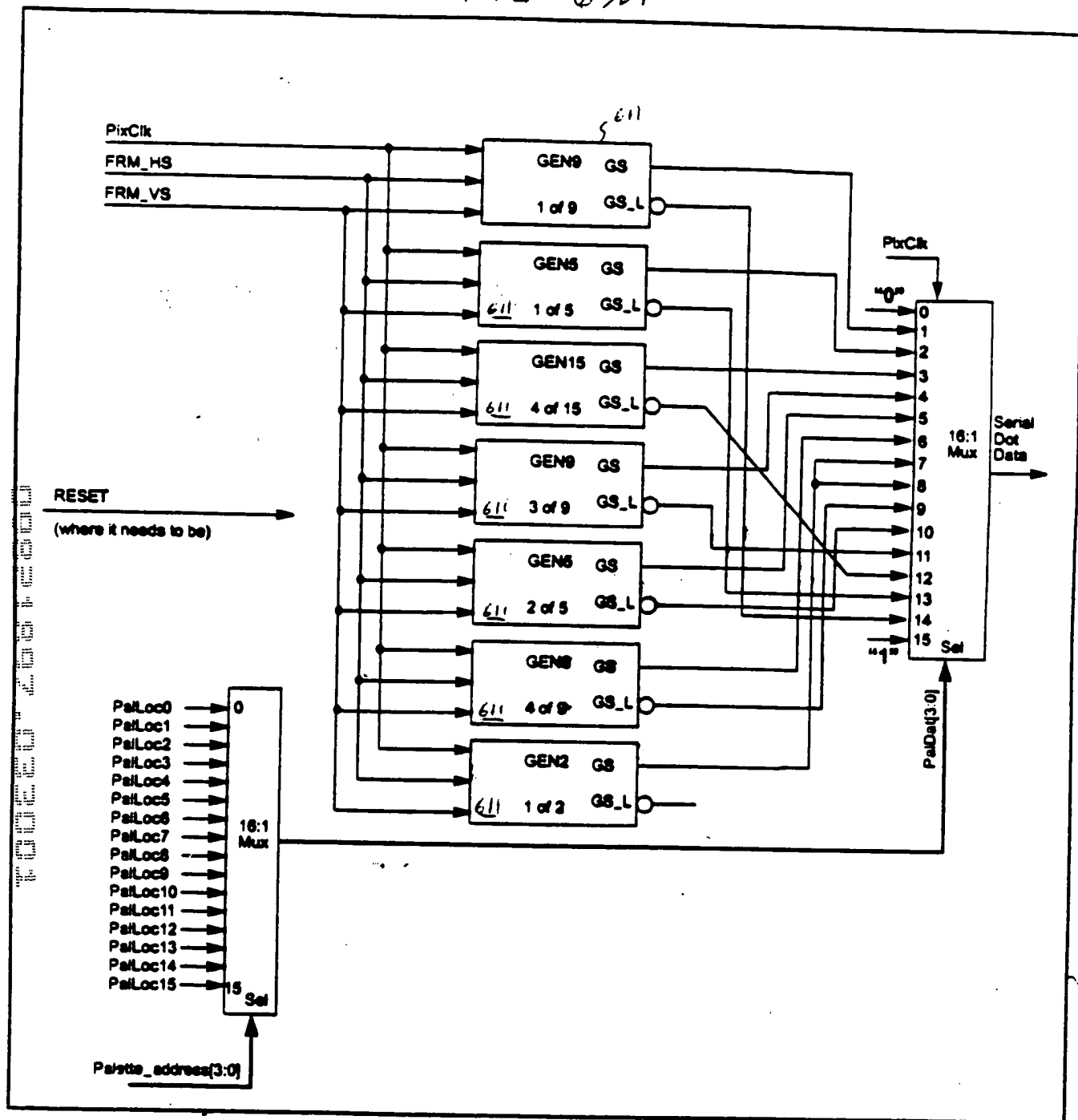
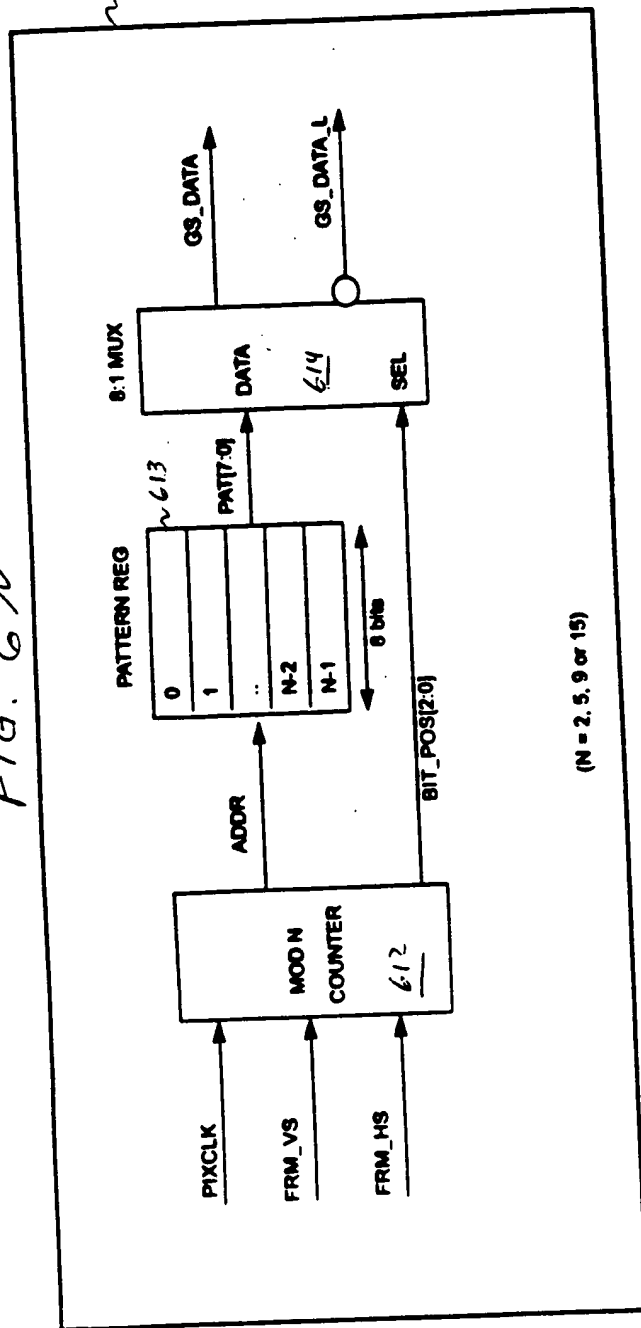


FIG. 6N



(N = 2, 5, 9 or 15)

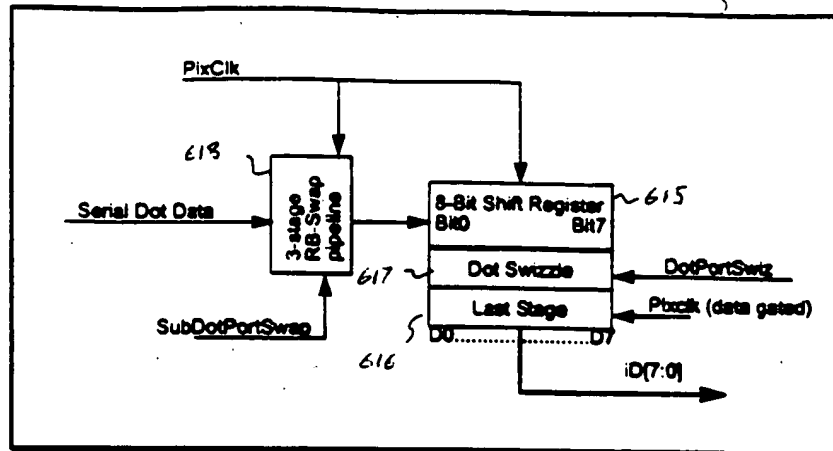
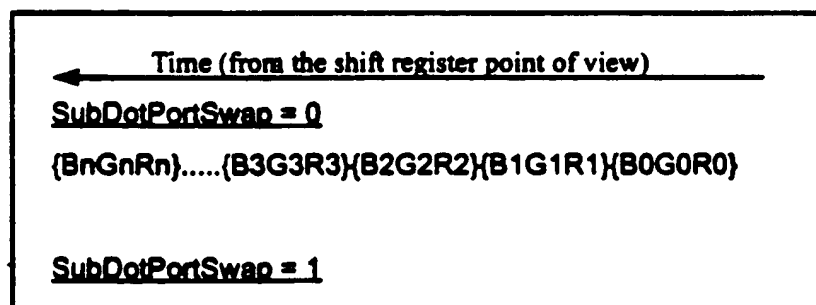


FIG 60

FIG. 6P



46872550

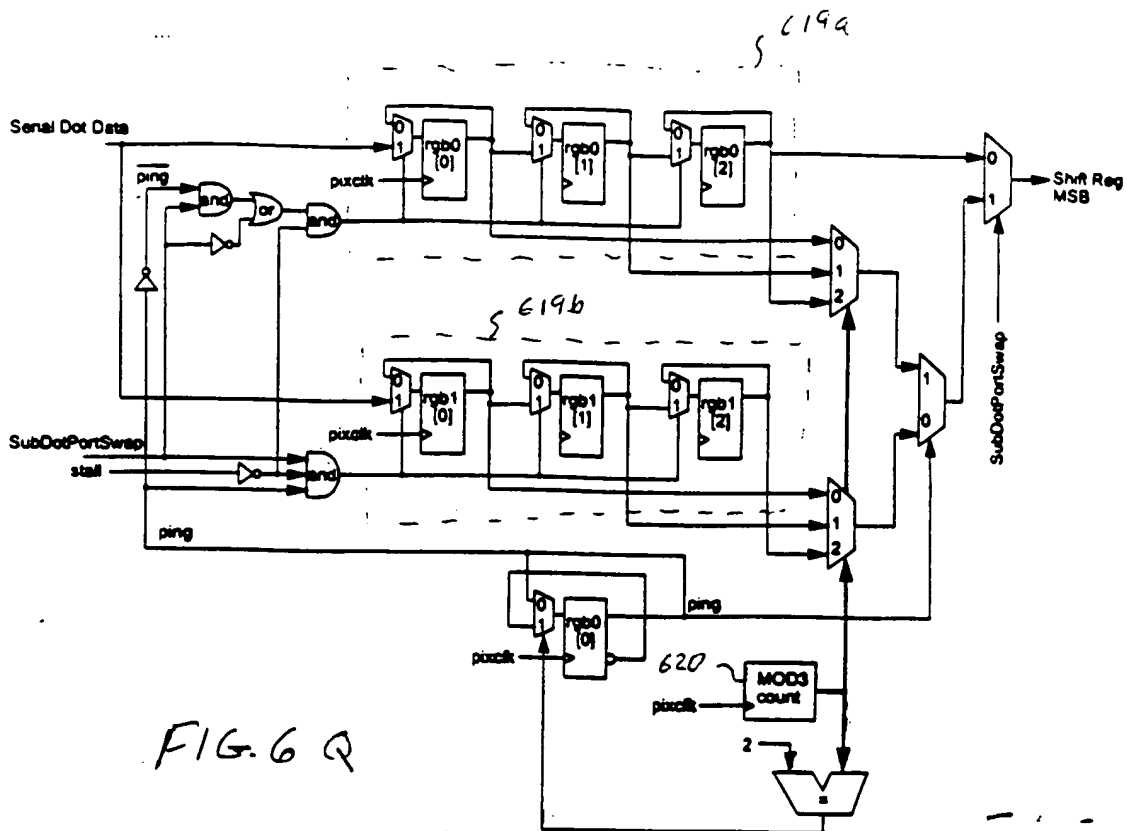


FIG. 6 Q

FIG 6.R

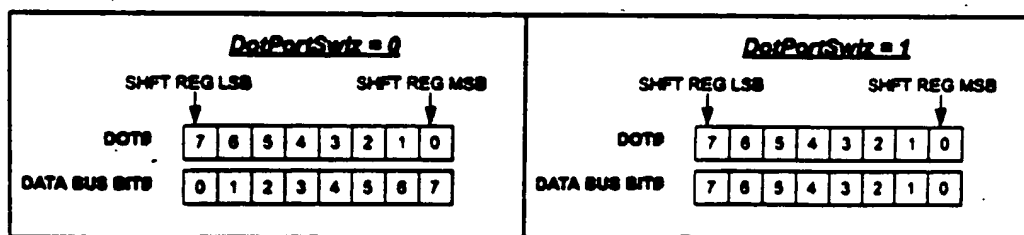
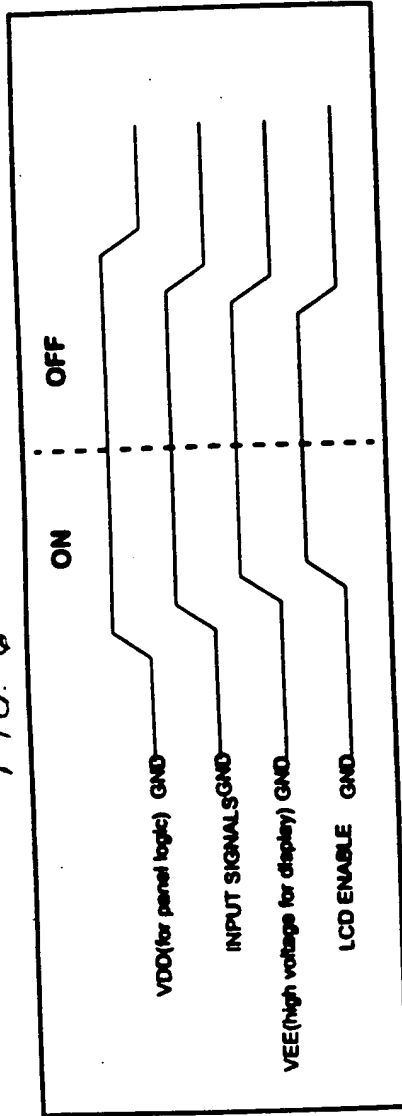


FIG. 6 S



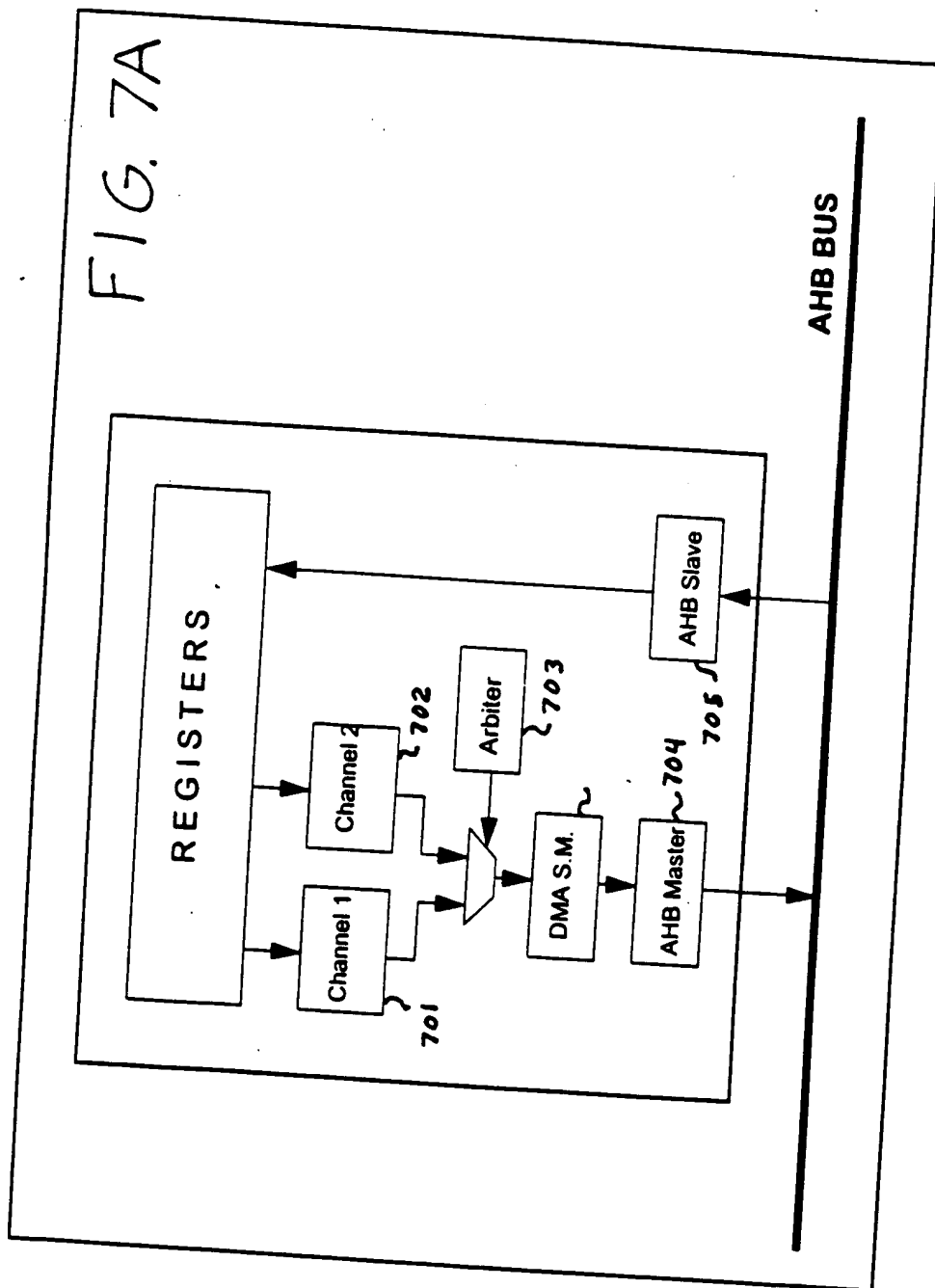


FIG. 7B

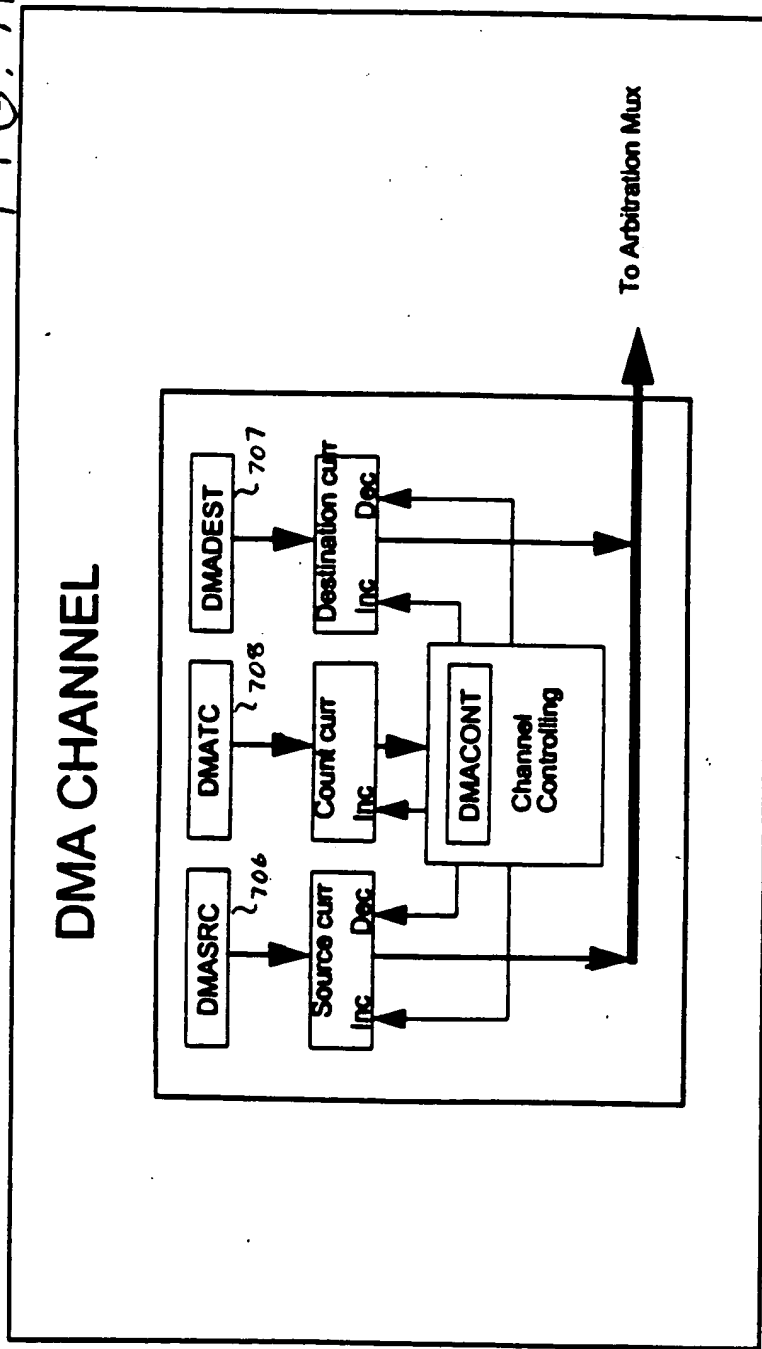
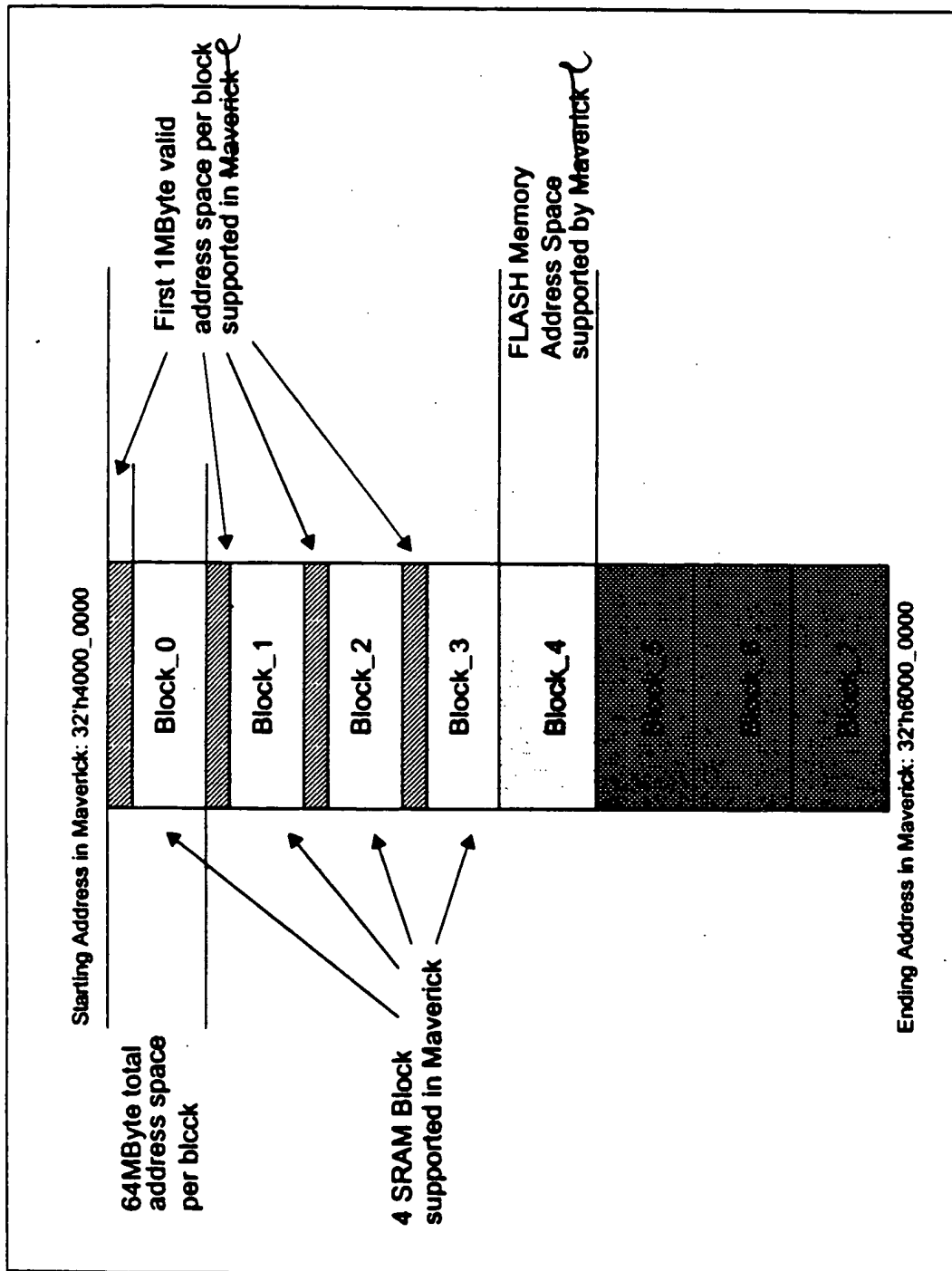
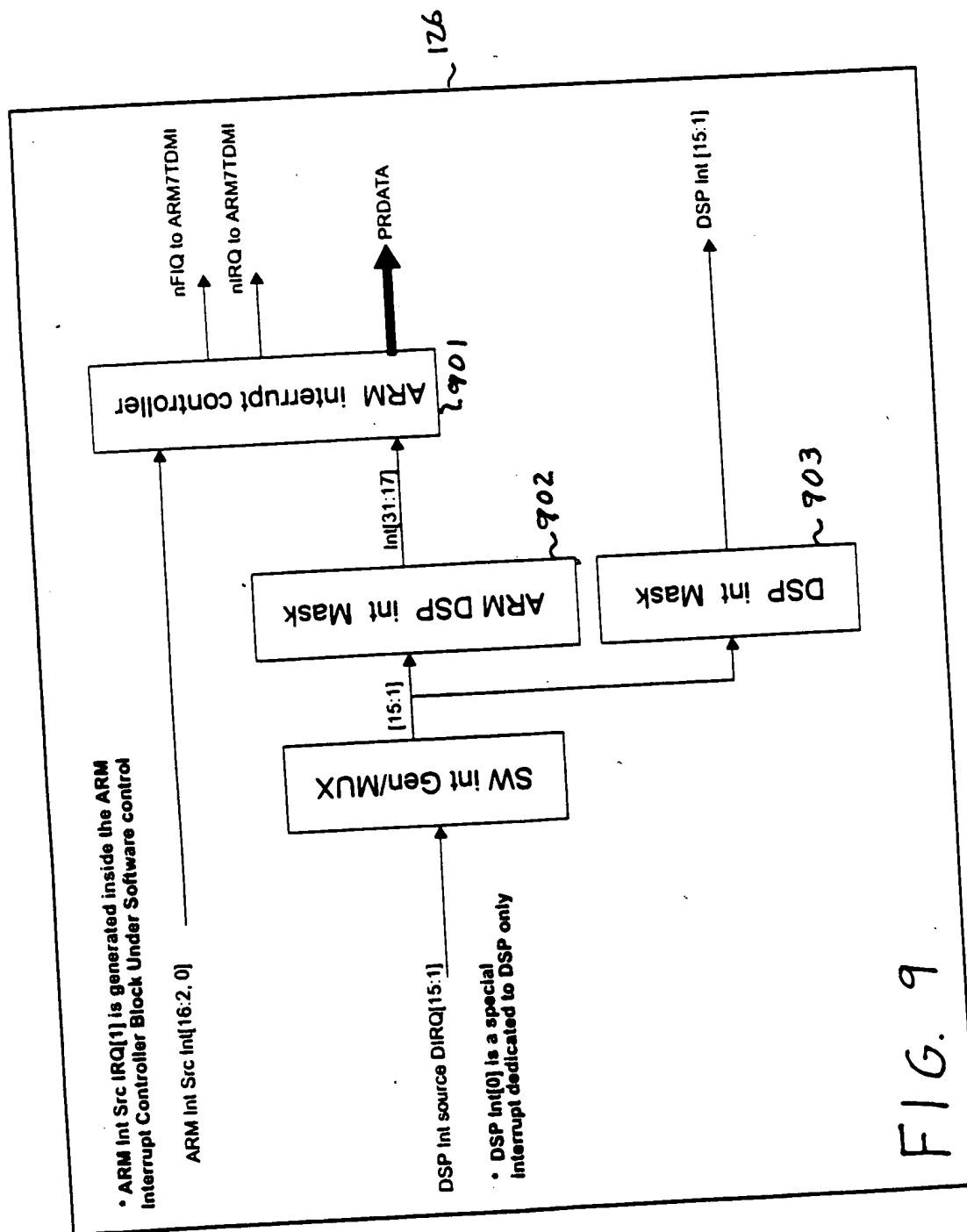


FIG. 8





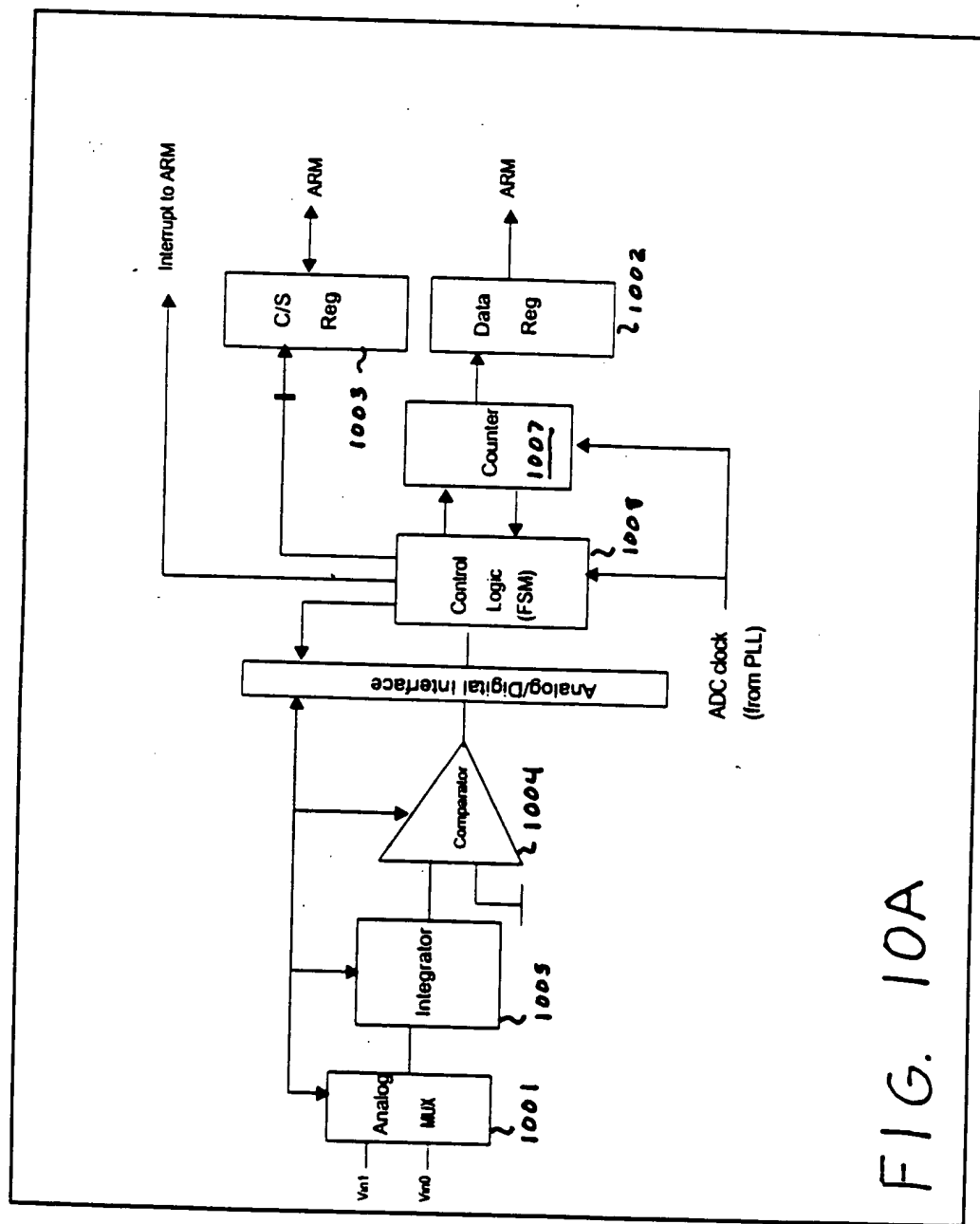


FIG. 10A

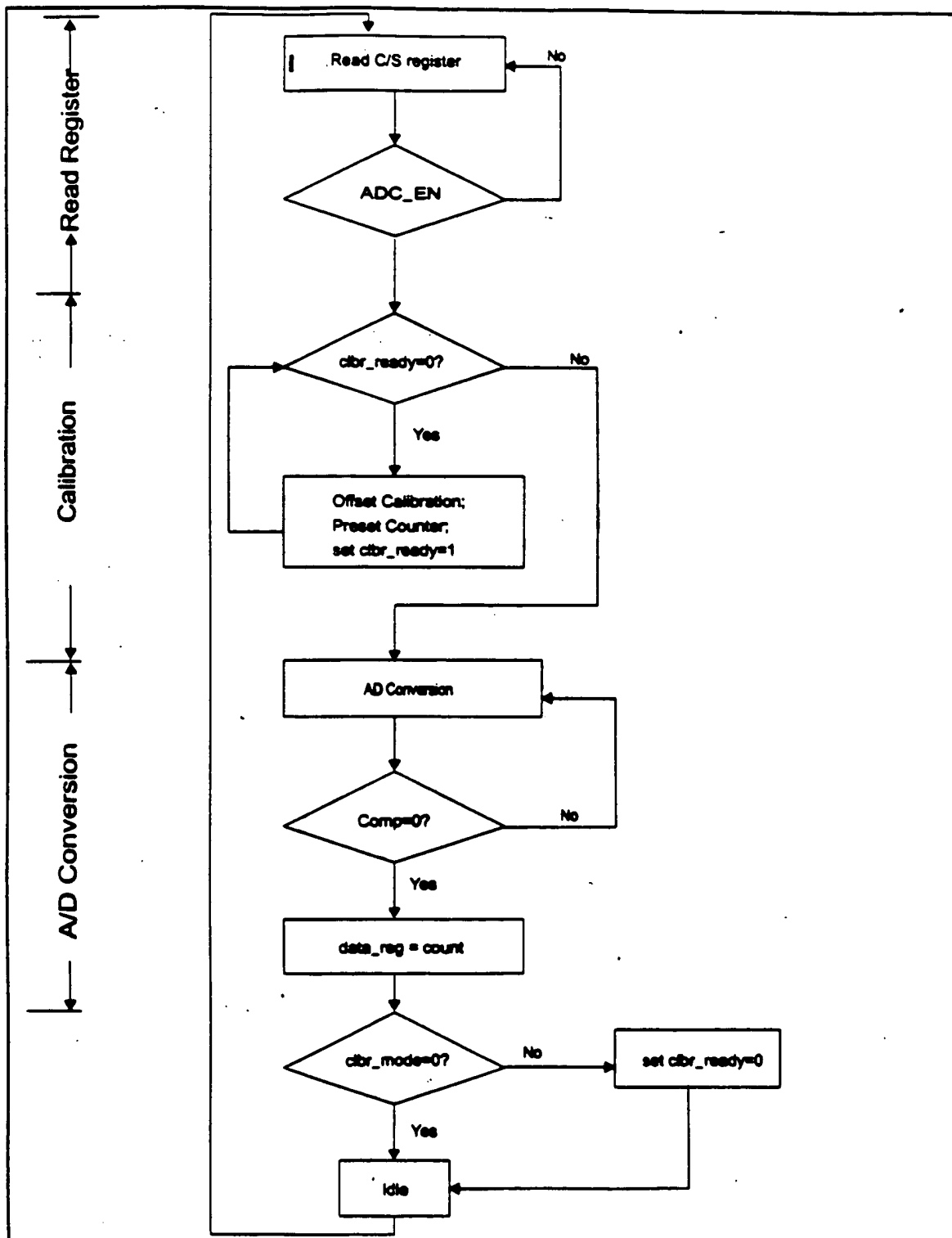
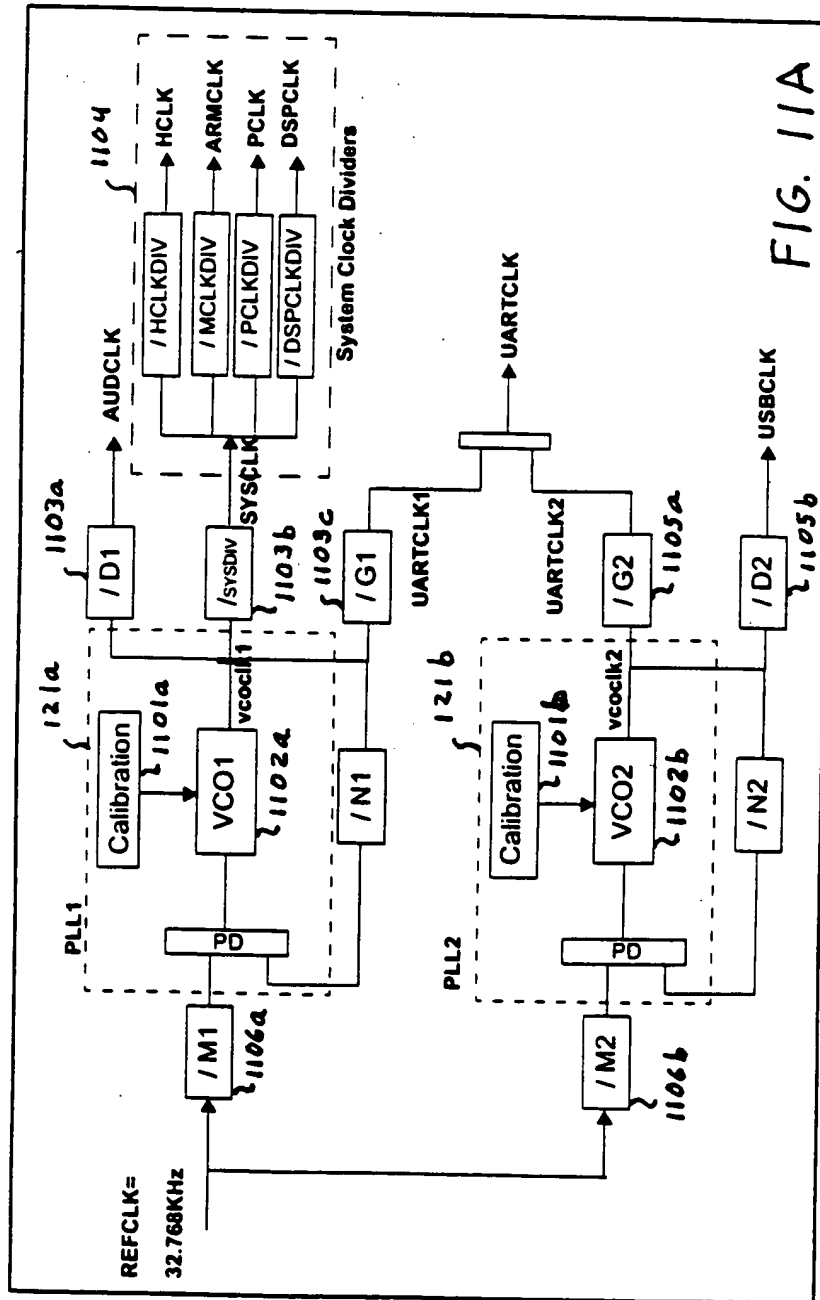


FIG. 10B



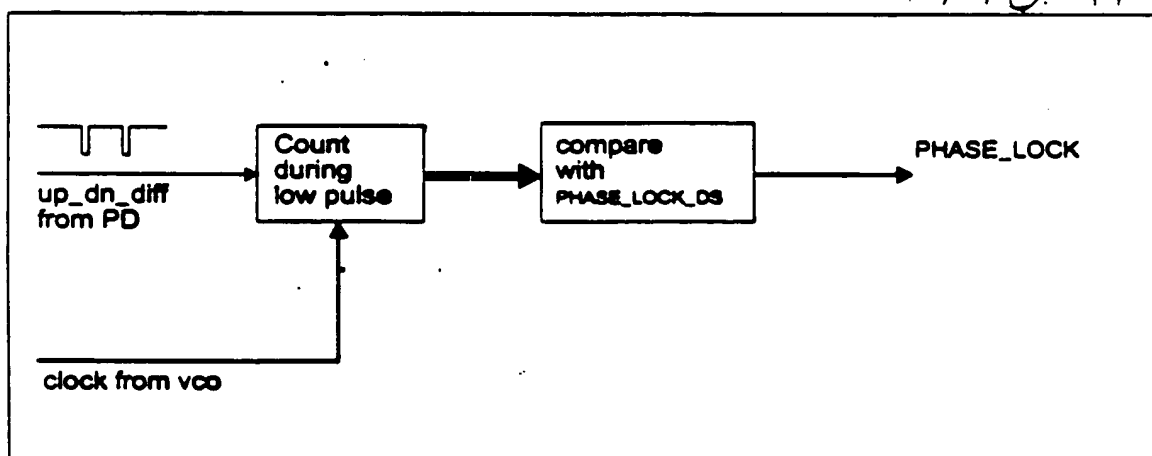
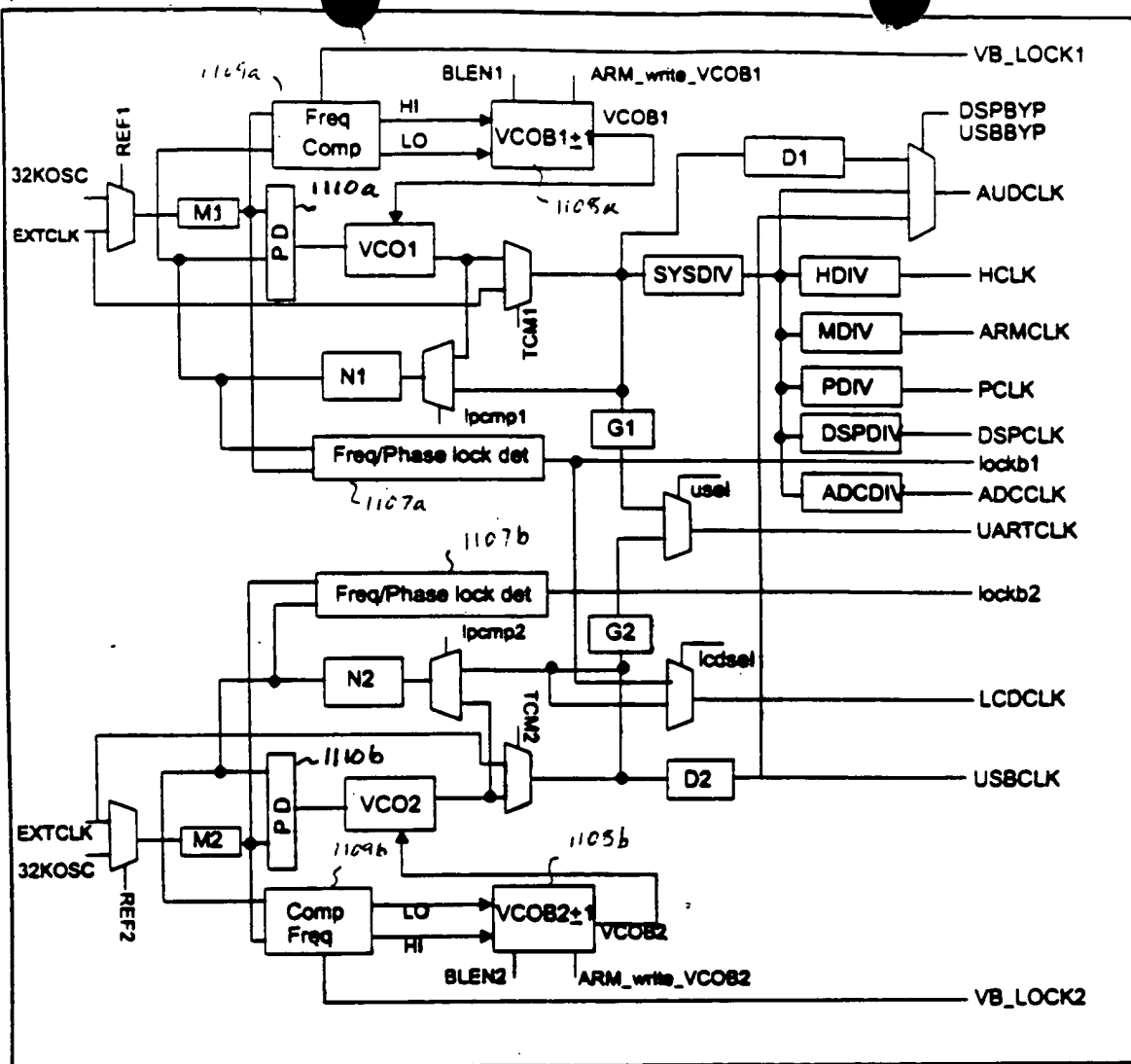
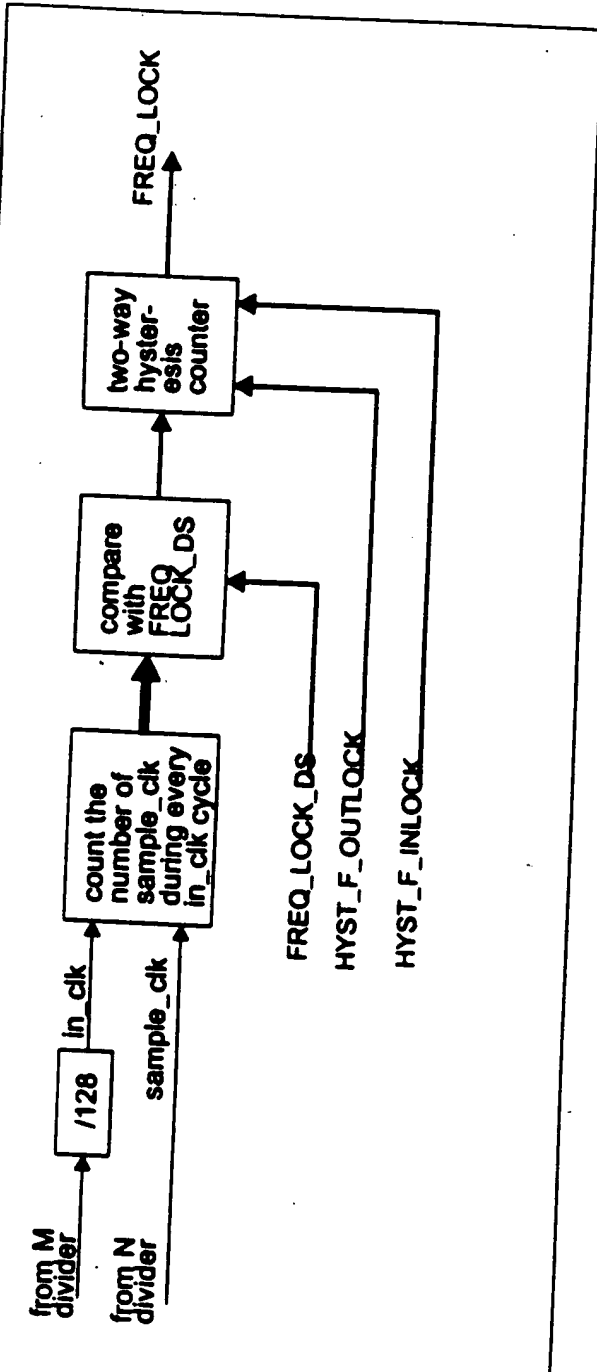


FIG. 11D



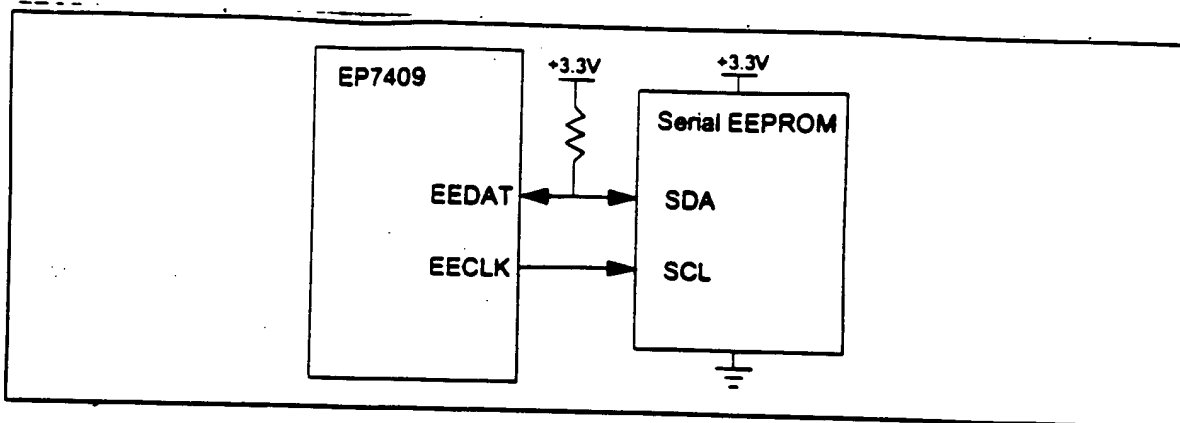


Fig. 12 A

Fig 12 B

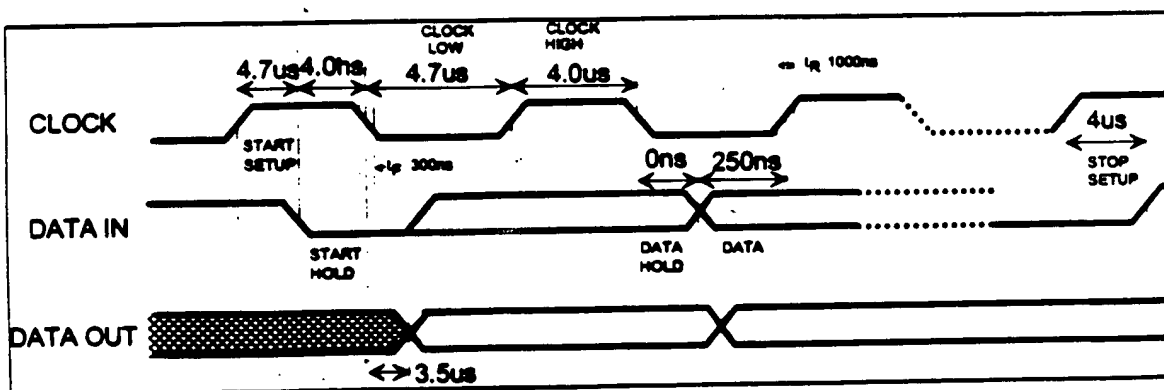
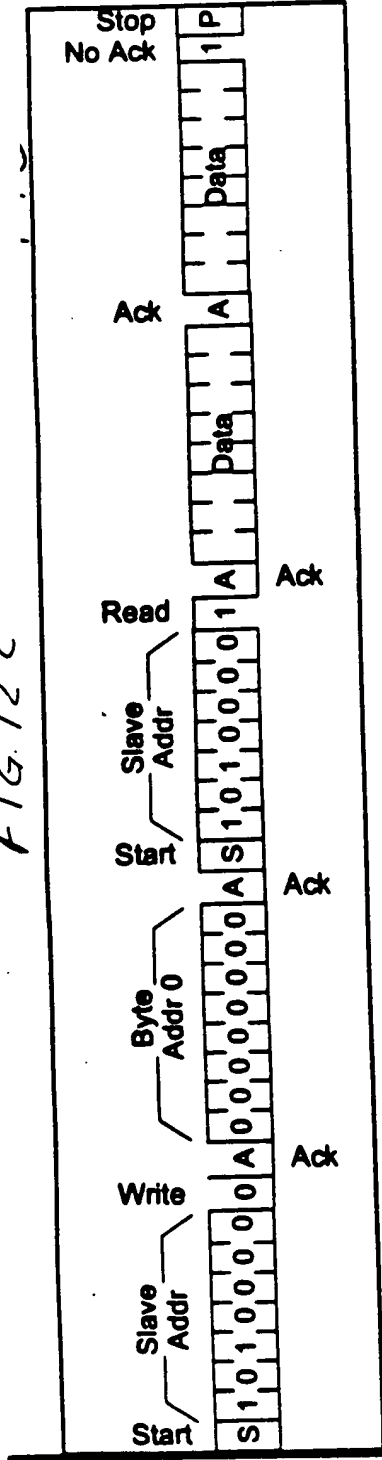


FIG. 12C



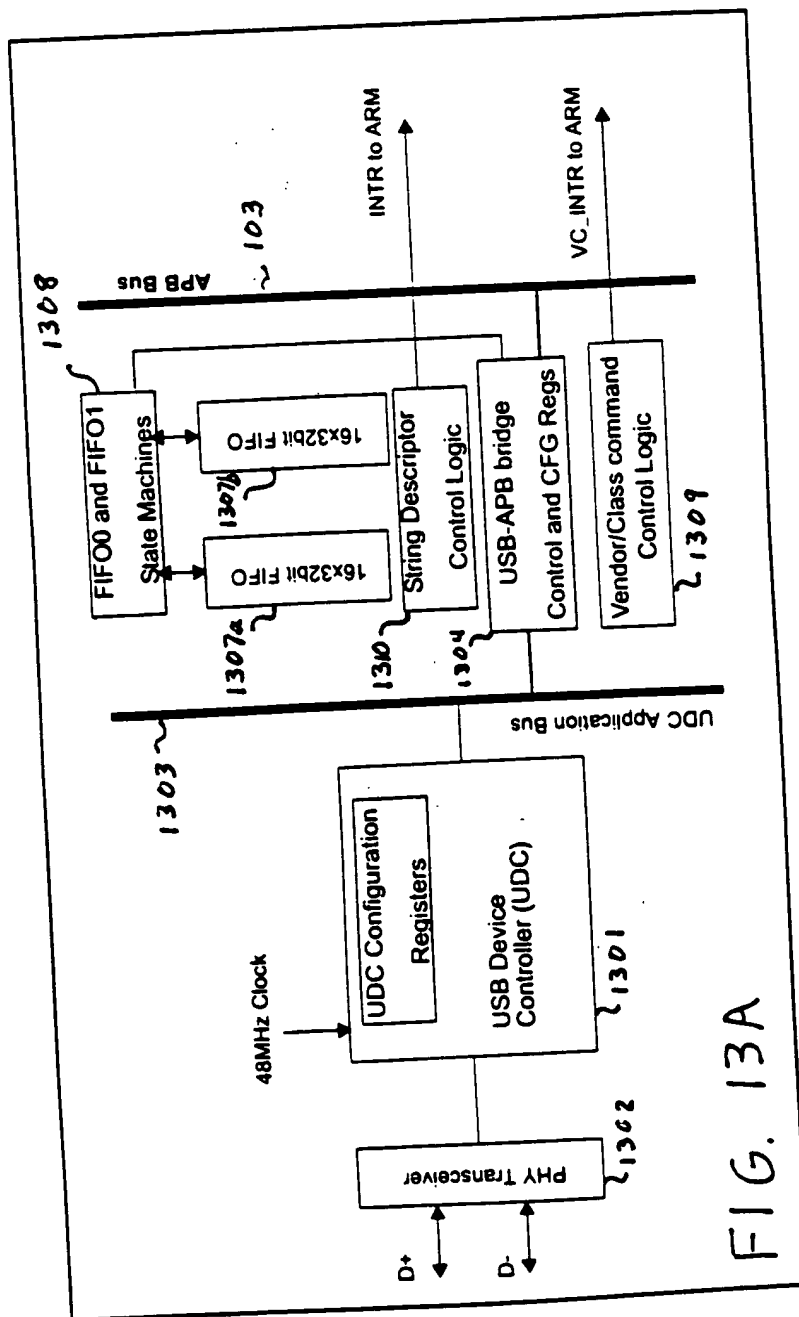


FIG. 13B

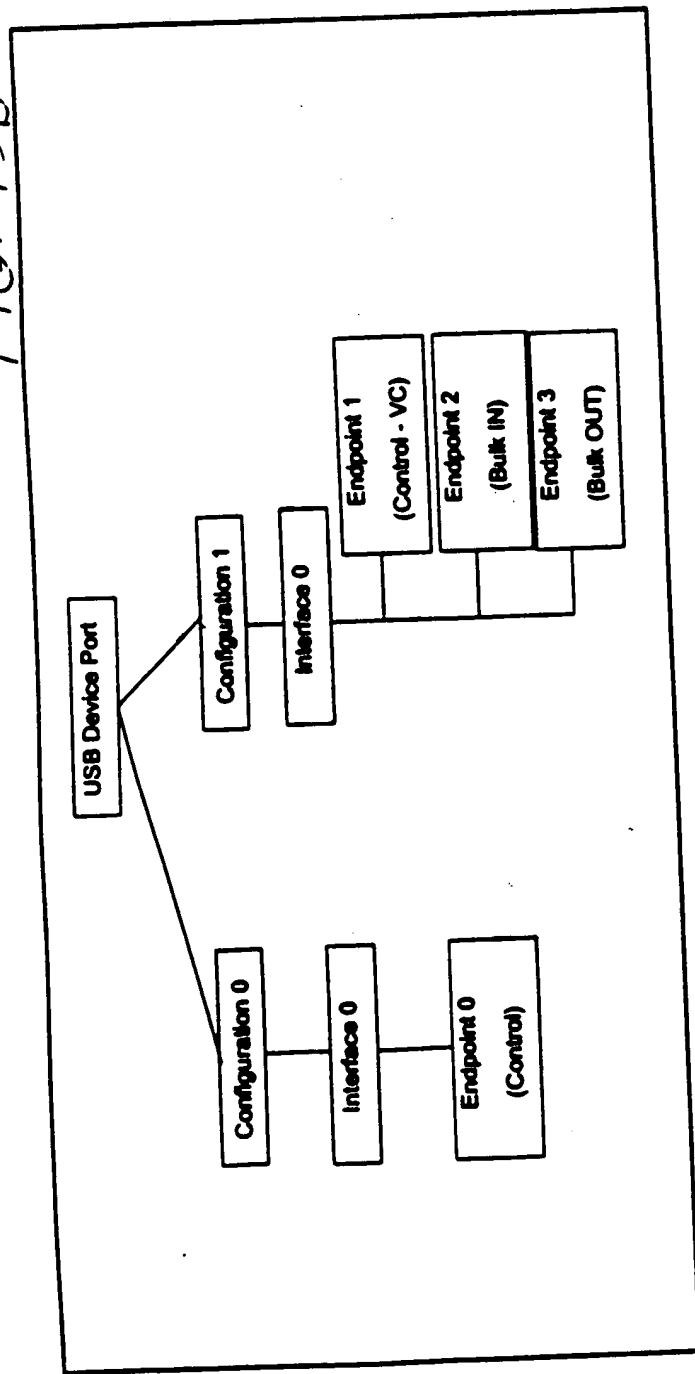
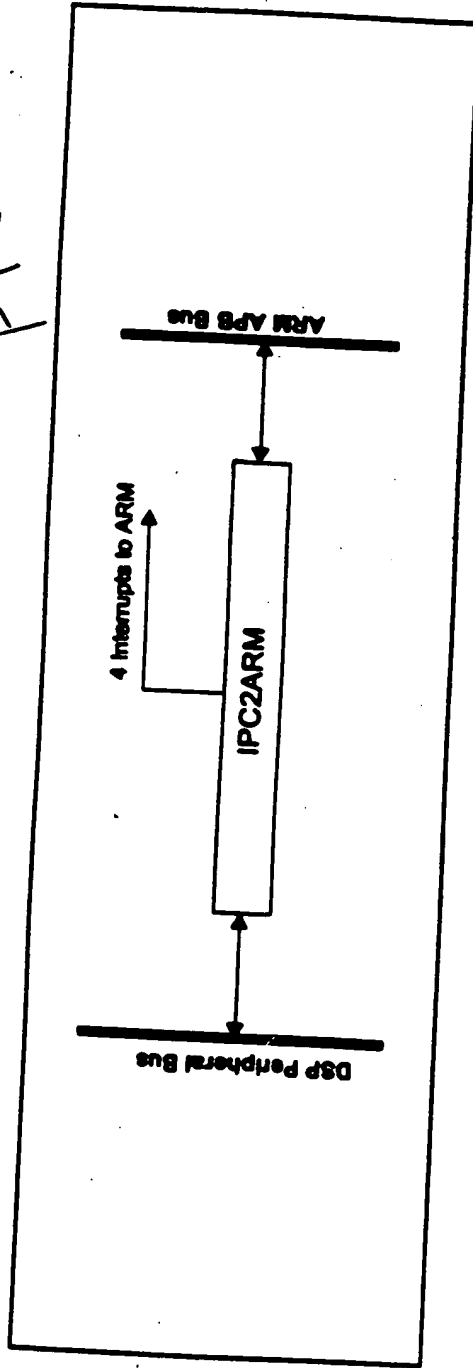


FIG. 15



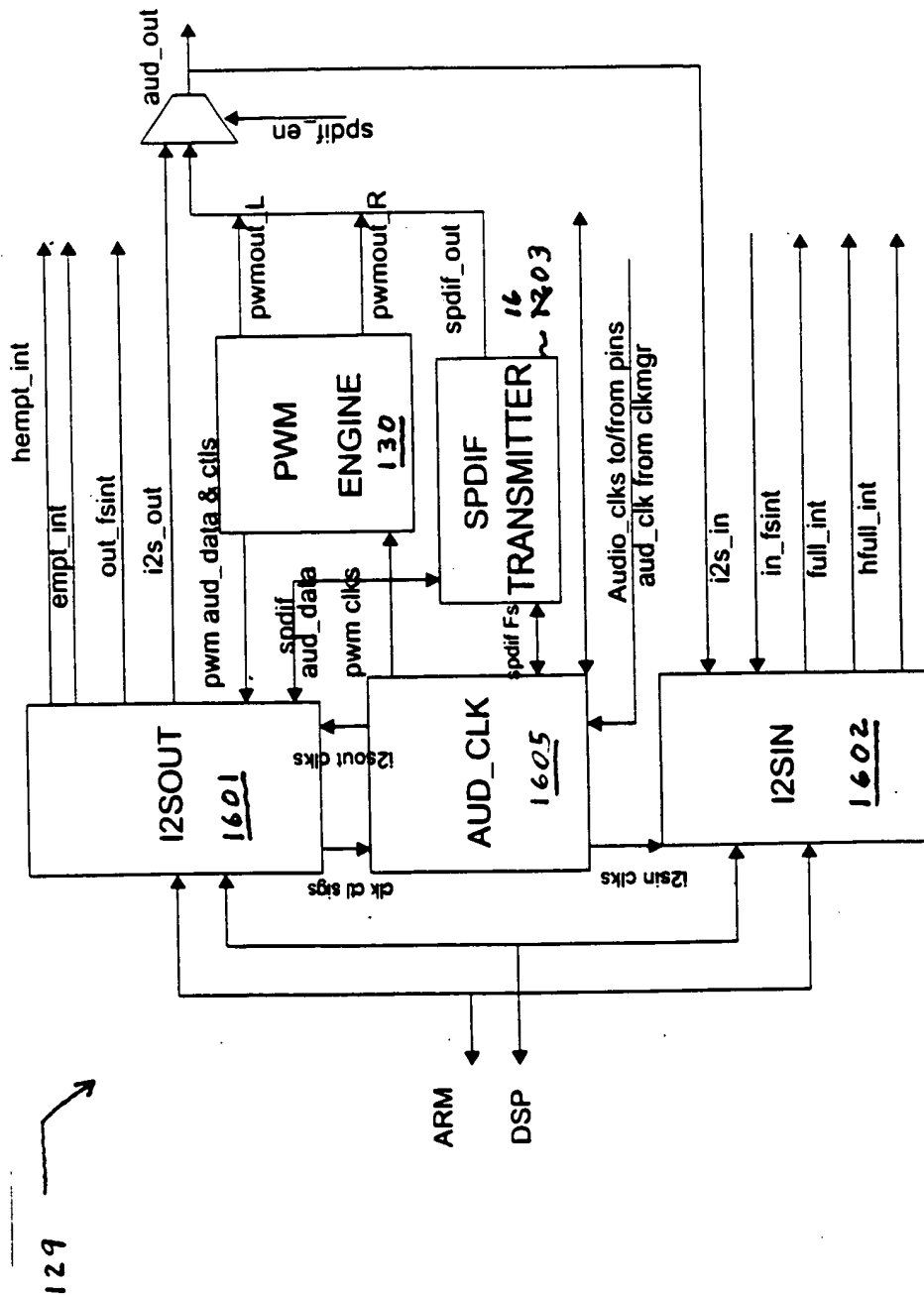


FIG. 16A

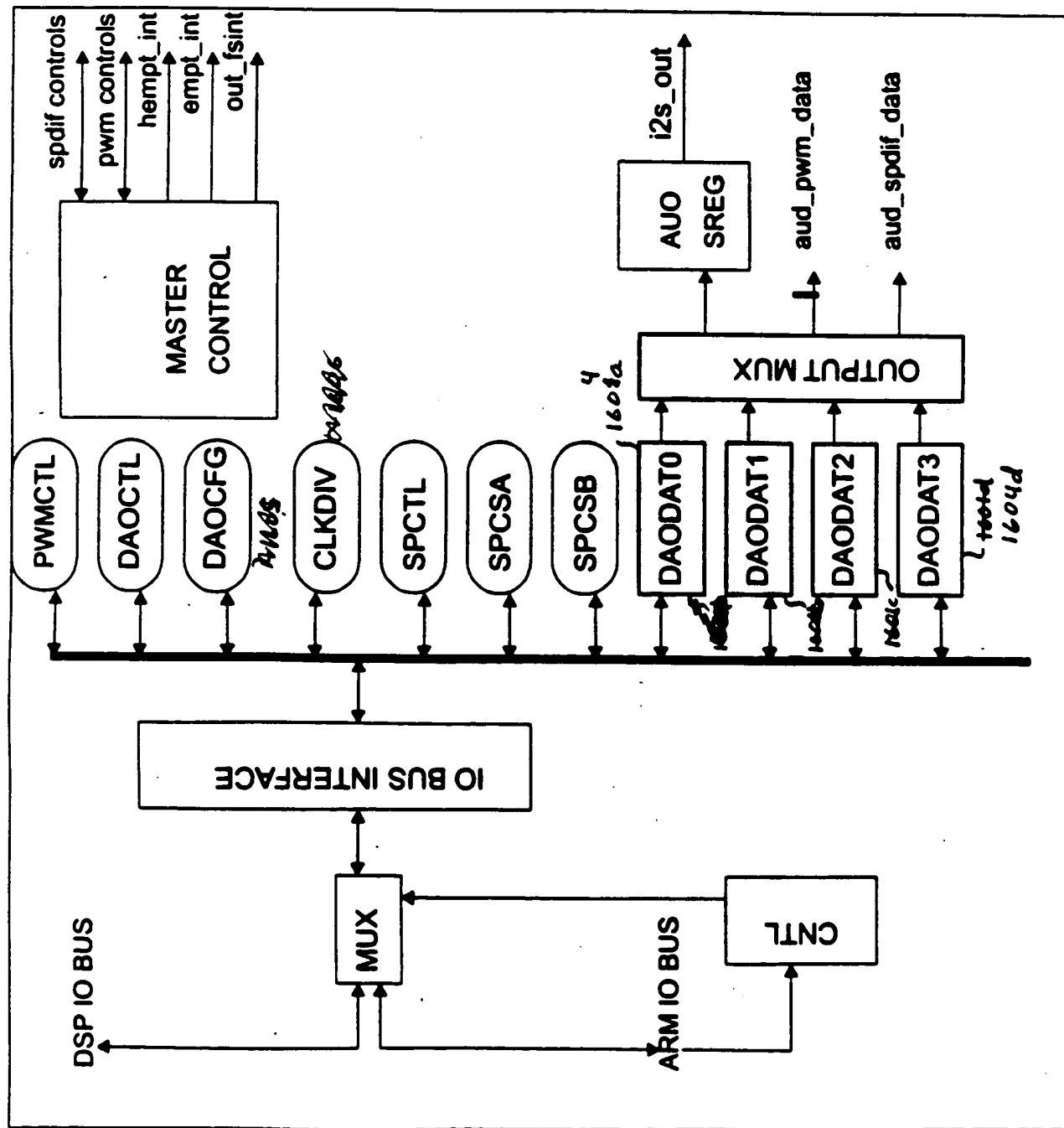


FIG. 16B

FIG. 16C

The following figure shows a typical output waveform.

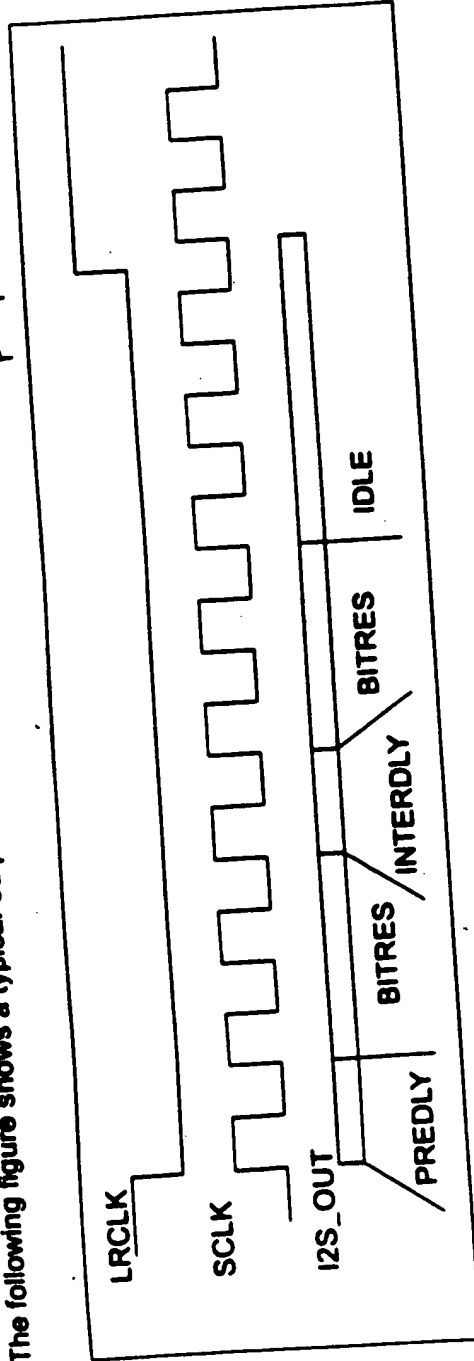


FIG. 16D

1602

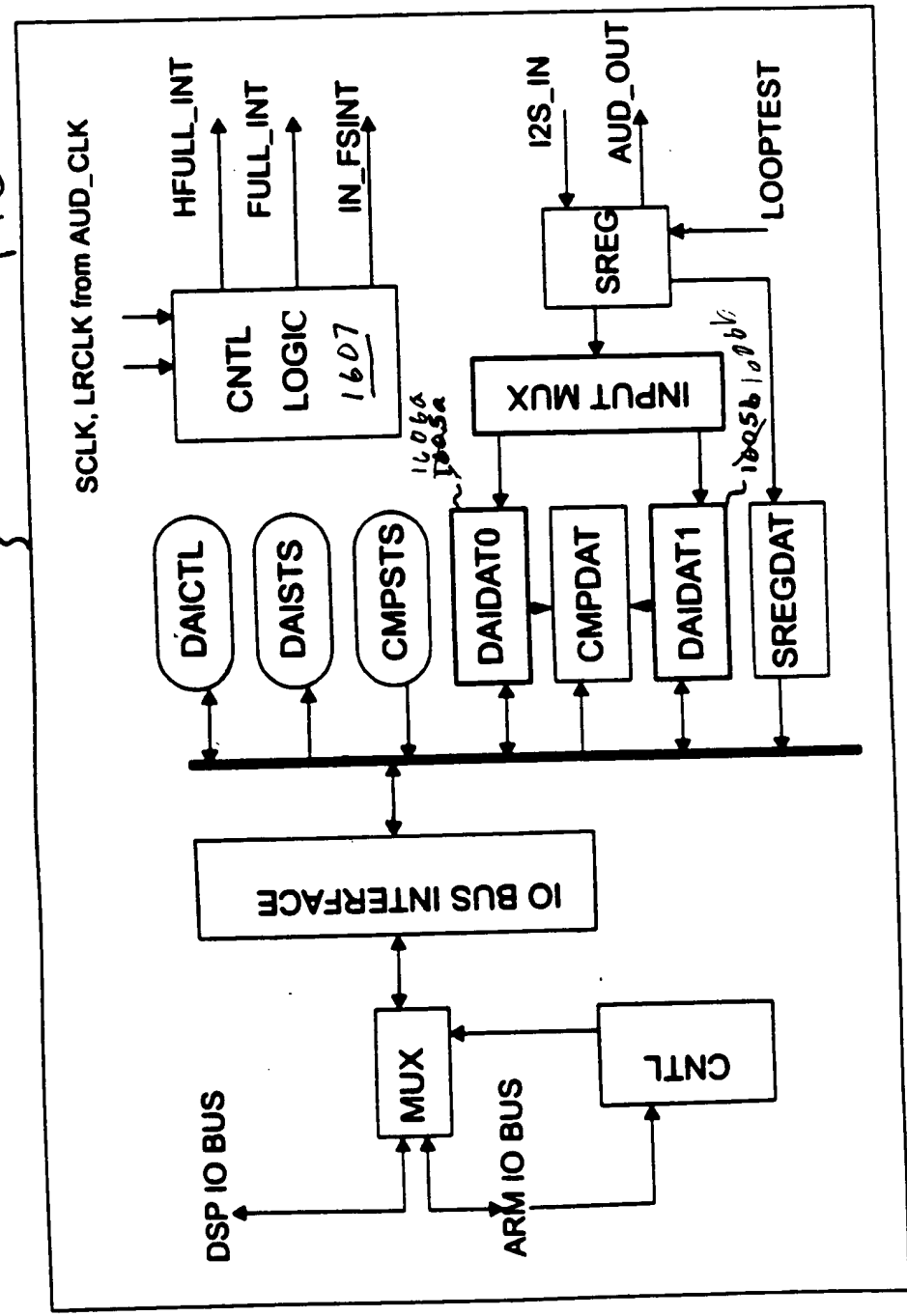


FIG. 17

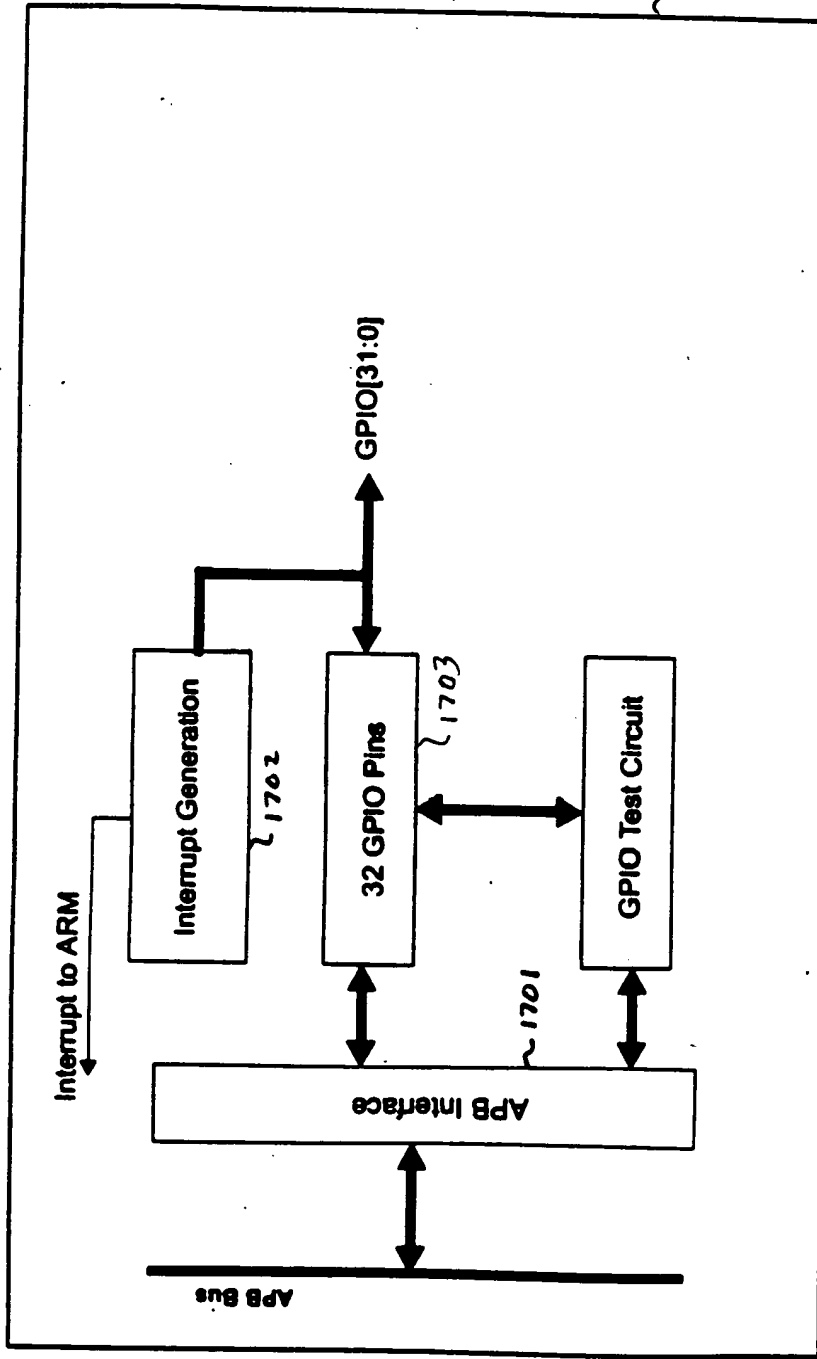


FIG. 18

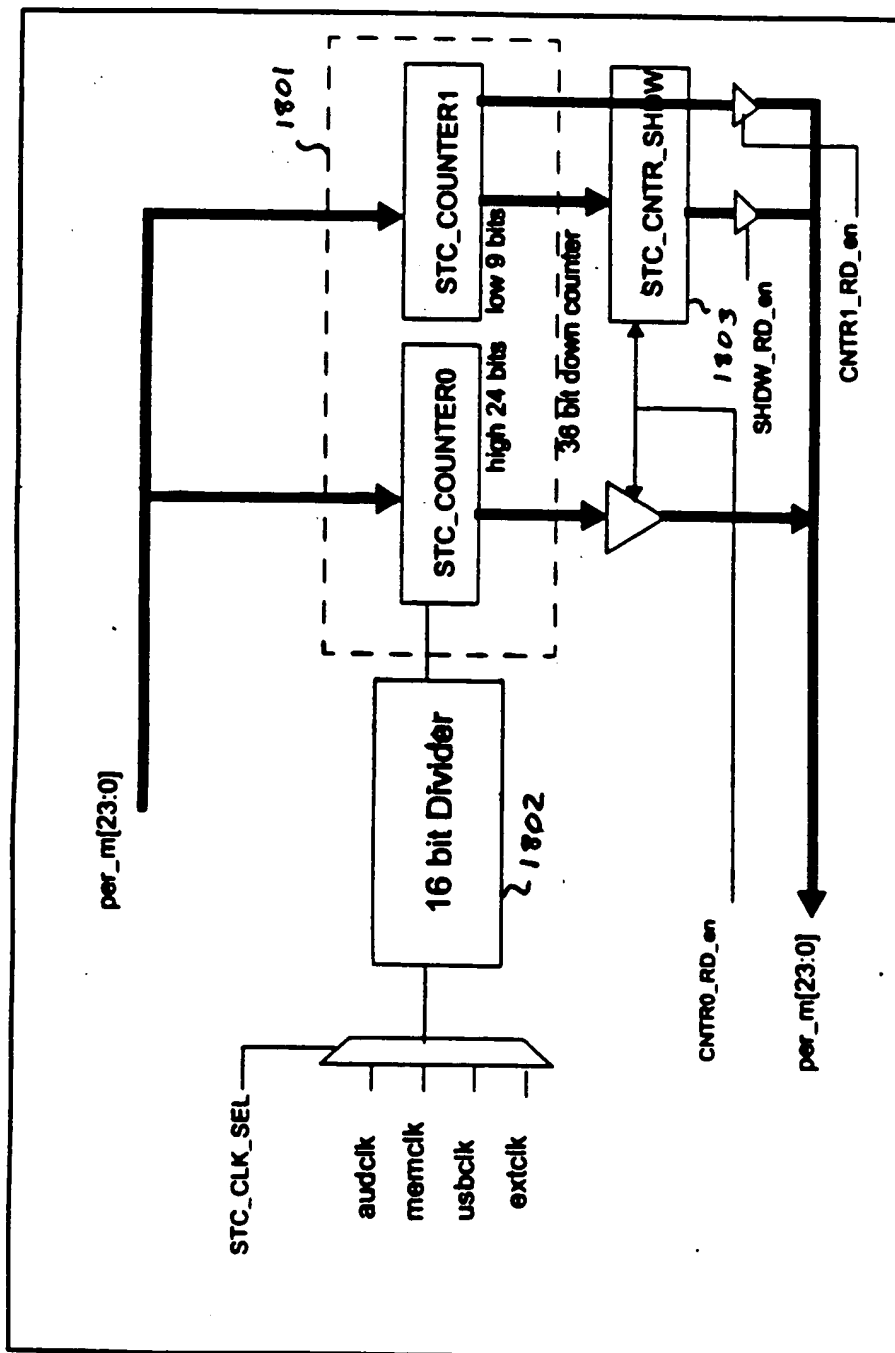


FIG. 19A

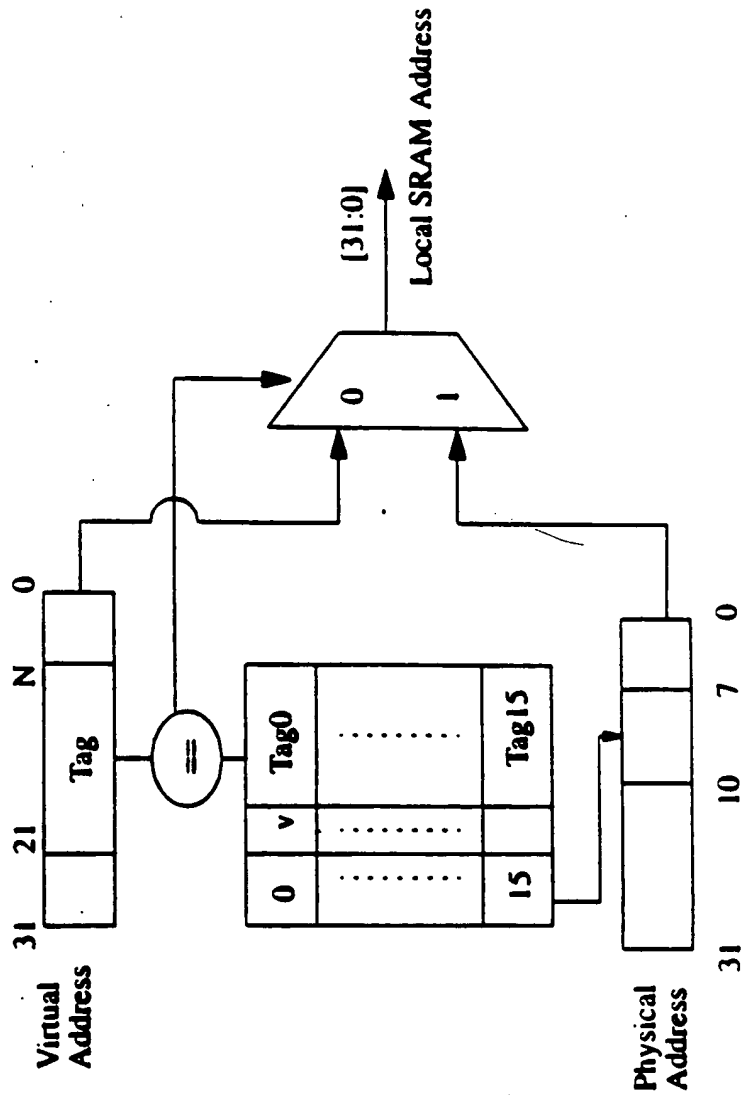


FIG 19B

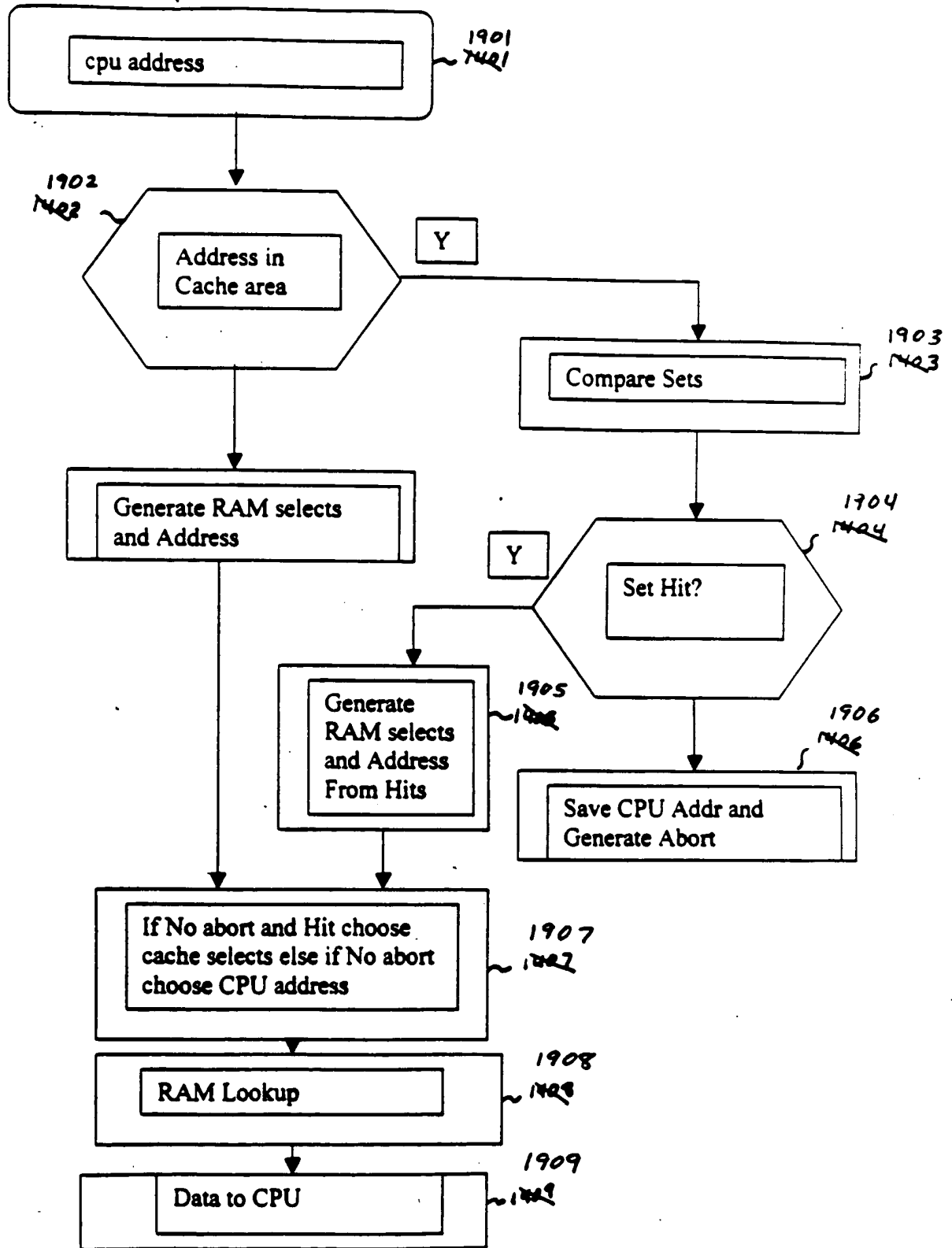


FIG 19B

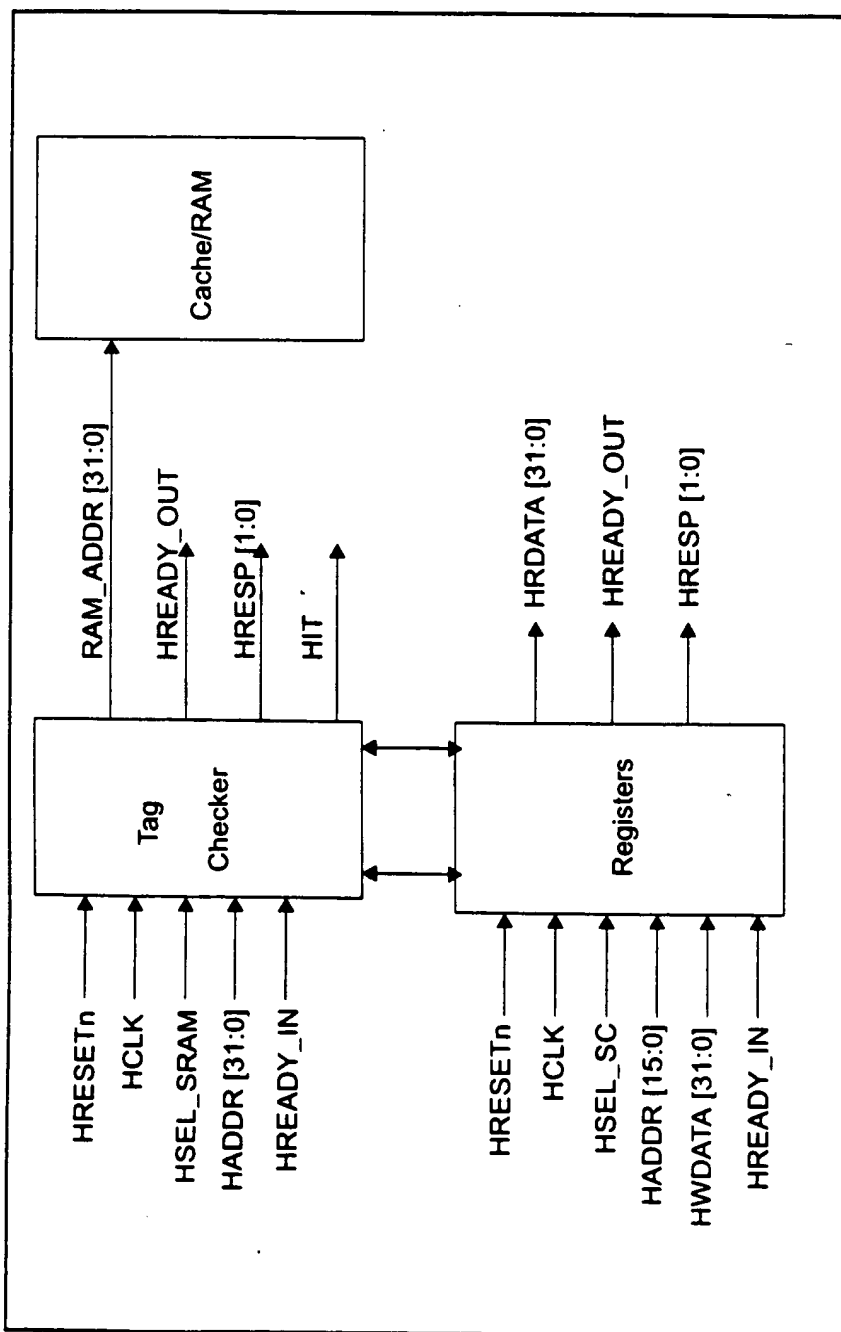


FIG. 19C

Fig. 20A

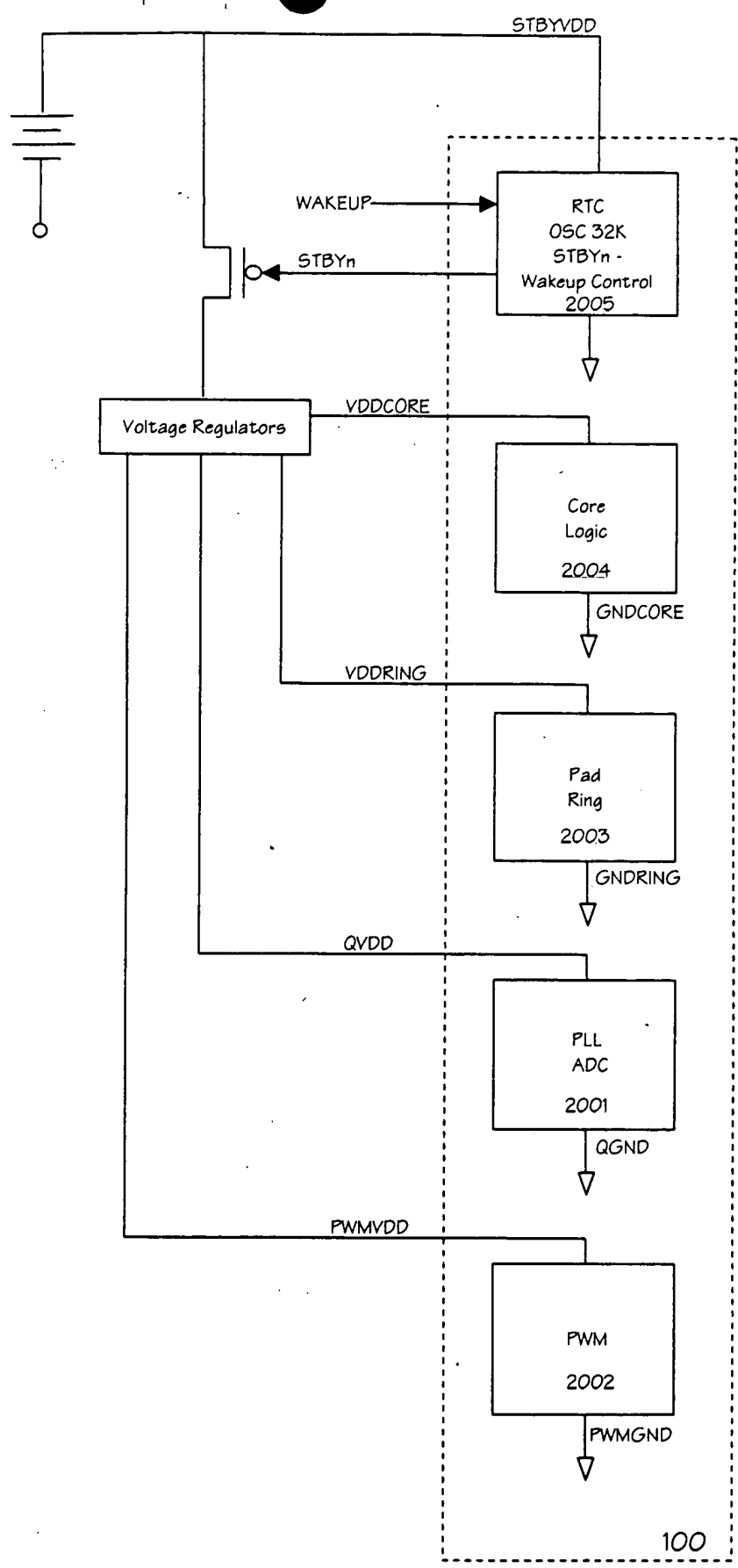
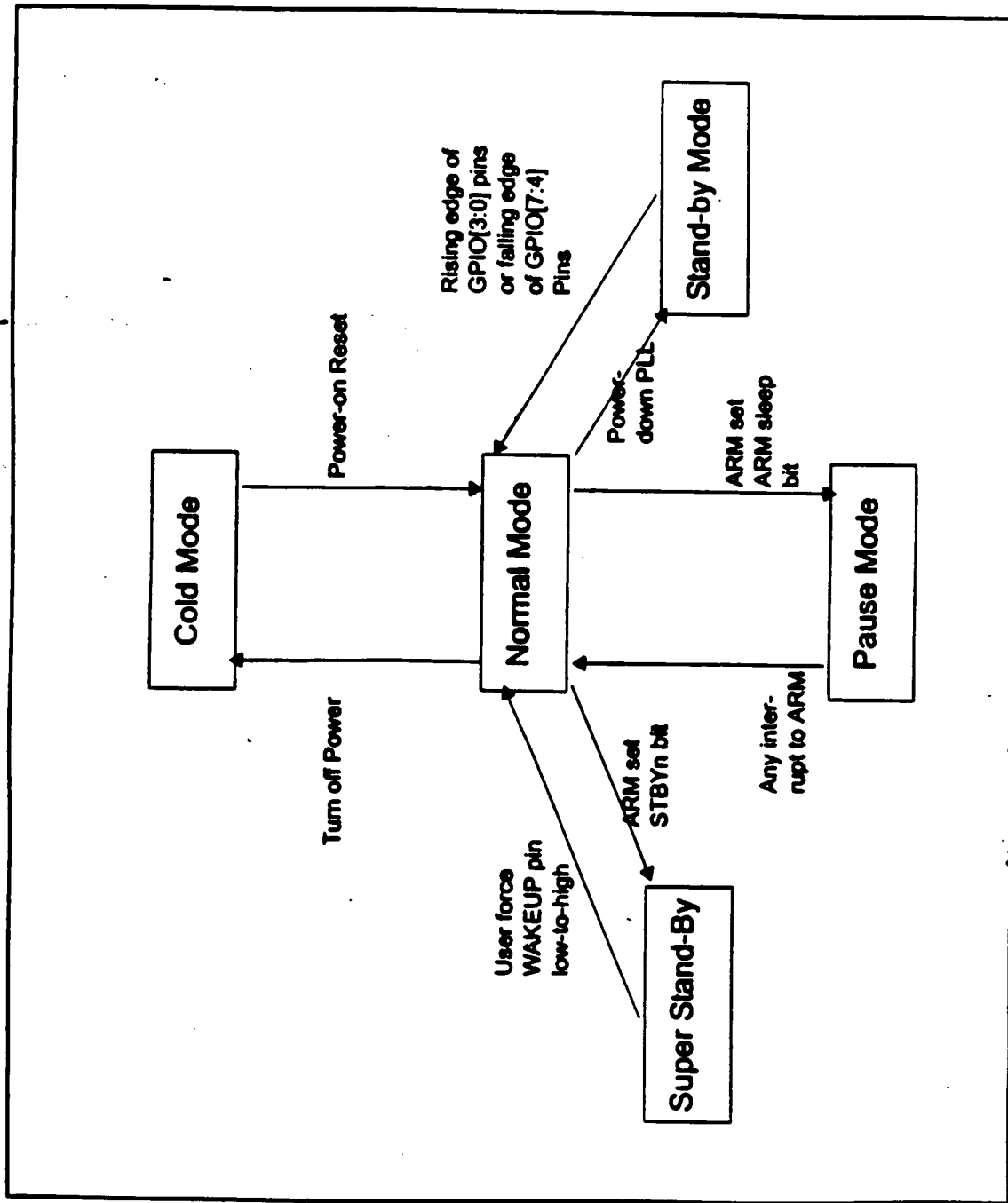


FIG. 20A

FIG. 20B



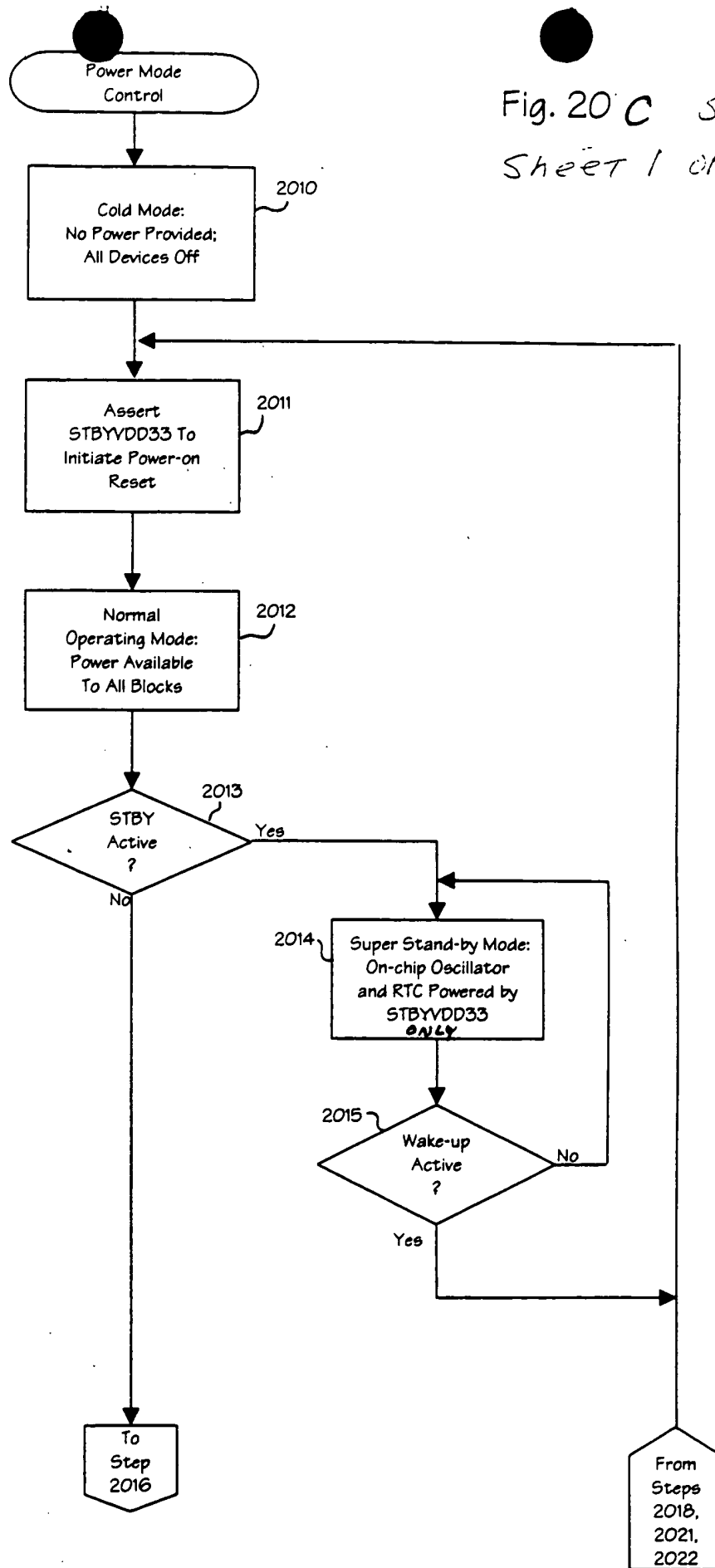


FIG. 20C
Sheet 2 of 3

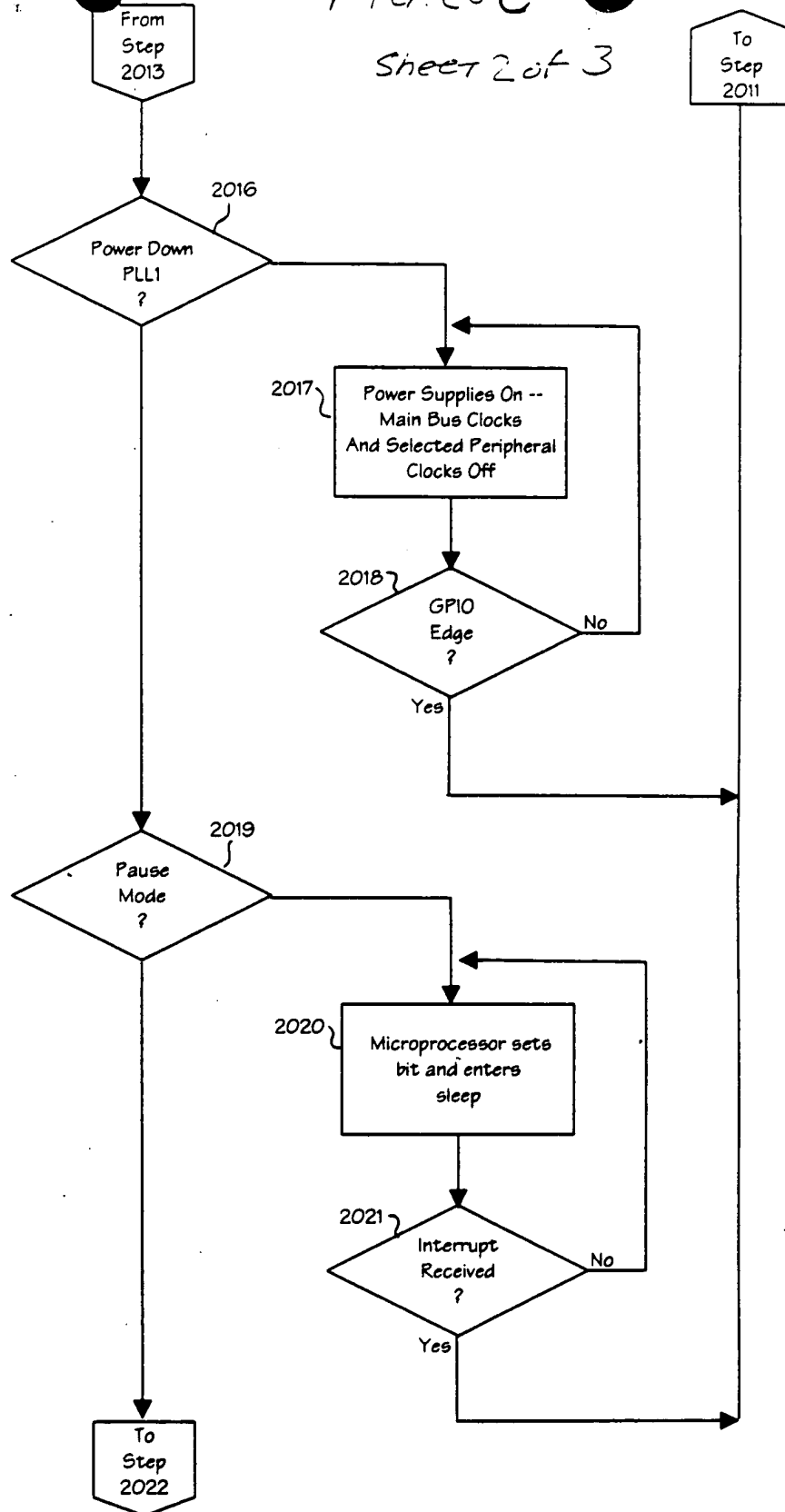


FIG. 20C

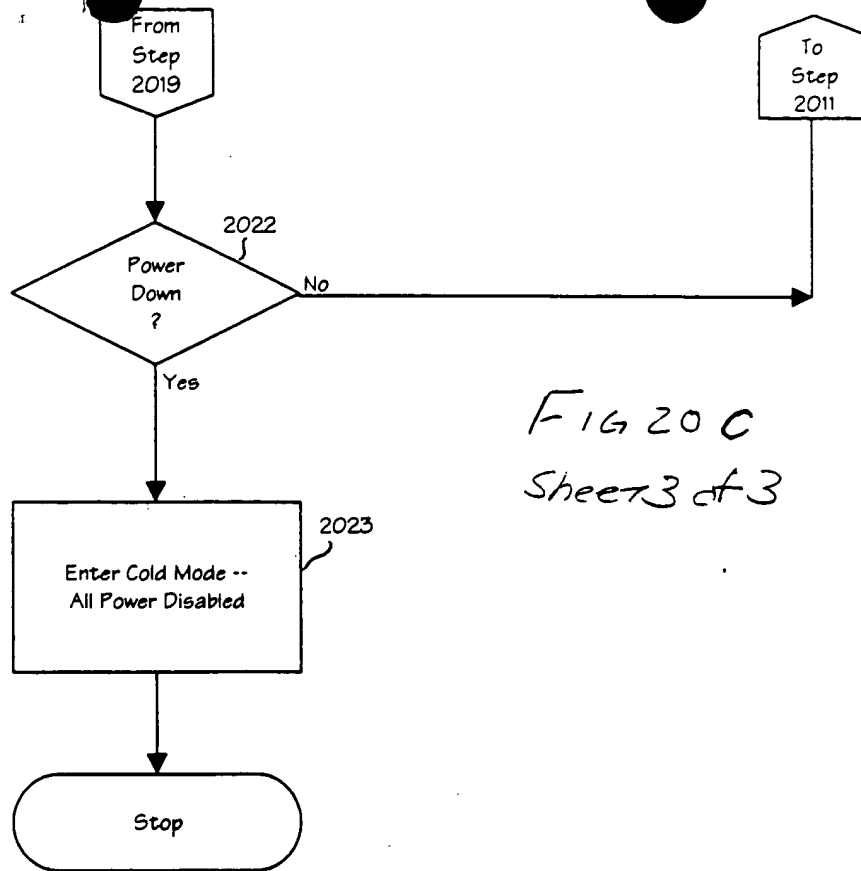
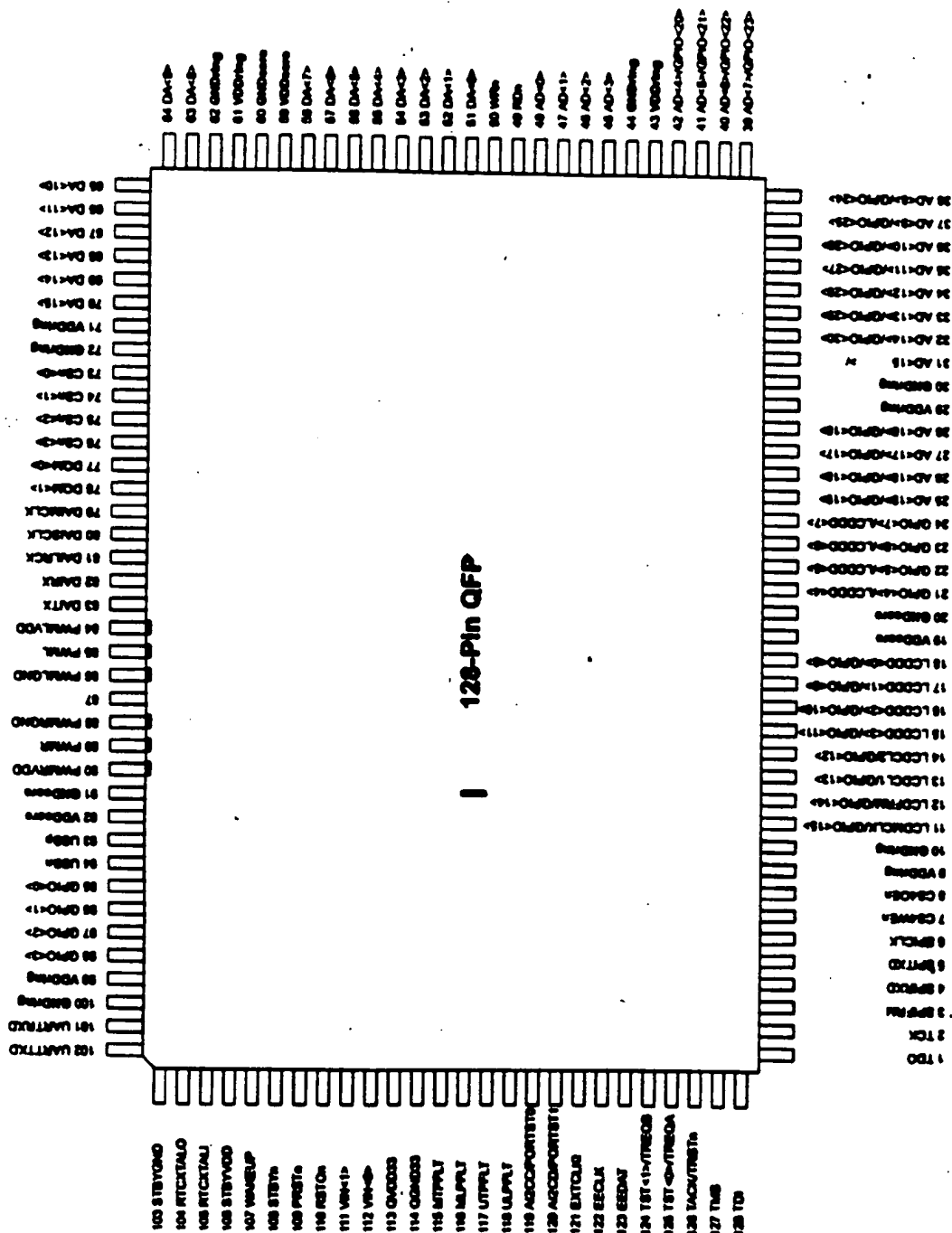


FIG 20C
Sheet 3 of 3

FIG 20C Sheet 3 of 3

FIG 21



* Pins in BOLD are double bonded.
* Some pins are internally bonded which are not shown in this diagram. Please